Integrating Smart Cities, Intelligent Systems, and VLSI Technology for India's Digital Growth

Pranav Parmar [1]* ,Meera Shroti[2],Maanvi Madhusoodhan[3] ,Riya Vakte[4]

[1,4]Computer Science and Engineering (Artificial Intelligence and Machine Learning), Acropolis Institute of Technology and Research, Indore, India ,
[2,3]Humanities , Acropolis Institute of Technology and Research, Indore, India
pranavparmar250290@acropolis.in *[1], meera.shroti@acropolis.in [2], maanvimadhusoodhanan@acropolis.in [3], riyavakte250785@acropolis.in [4]

Abstract. India's rapid urbanization is on a large scale and its Smart Cities project of a high-tech city concept has been launched at the same time as its VLSI and semiconductor development. The paper discusses the role of VLSI technology in Smart City systems by looking at urbanization trends expected in India, comparing the domestic "Made-in-India" Vikram-3201 microprocessor with global chips, reviewing recent investments in VLSI and fabrication, and analyzing the impact of U.S.-China tensions on Taiwan's chip industry. We take a look at the smart cities of the world that are at the top of the list for their use of VLSI/IoT technologies and show the innovative works of such cities as Singapore and Barcelona. The accomplishments and drawbacks of India's Smart Cities Mission are presented (34% of projects done, but only 18 of 100 cities completed all works planned). The work applies quantitative trend analysis (e.g., urban population growth rates) and visual representations, and aligns government successes with missed targets, delays, and cybersecurity issues. Moreover, the study portrays the job market in India's VLSI sector (e.g., ~12,000 fresh graduates recruited every year, in addition to ~25–30K employment opportunities arising from major new fab projects). Lastly, the study identifies significant challenges (budget limits, logistics issues, skills gaps) along with new technologies (such as digital twins and 6G) likely to affect future smart city development. The integrated review leverages data from UN/World Bank, industry reports, and scholarly sources to deliver an all-encompassing, data-supported analysis of VLSI technology and Smart City growth in India.

Keywords- VLSI(very large scale integrating), Smart Cities, Urbanization, Semiconductor Industry, Vikram-3201, IoT, Digital Twins, 6G, *Taiwan Chip Dominance, India Semiconductor Mission* .

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1 Introduction

The urban population of India is climbing very quickly, and simultaneously the nation is trying to obtain technology independence in semiconductors to support the Smart City ecosystem. This situation leads to a major conflict in terms of: can India's planning, infrastructure, and technology development cope with urban growth?

1.1 Problem Statement

How can India coordinate its urbanization, semiconductor/VLSI development, and Smart City implementation to the same levels as realistic projections and global benchmarks? Ultimately, these efforts must improve citizens' quality of life – new technologies should serve people's needs as cities grow .

In order to answer this question, the research looks at:

- The expected rise in urban population in India
- The local semiconductor projects such as the Vikram-3201 chip, and the comparison of its technology with that of the world's most advanced fabs
- Geopolitical issues that affect global chip supplies (like Taiwan's leading position, U.S.–China conflicts)
- India's Smart Cities Mission (SCM) progress and shortcomings illustrated by quantitative indicators such as:
- Annual growth rates
- Completion percentages
- Correlations (e.g., city GDP vs. smart-performance index)

1.2 Approach Used

The study uses a structured, mixed-method research methodology: Quantitative Analysis

- Population growth trends, annualized growth rates, proportional comparisons
- Descriptive correlations (e.g., city GDP vs. smart-index performance)

1.3 Qualitative Analysis

- Scrutiny of the policy underlying the Smart Cities Mission
- Evaluation of semiconductor projects, their geopolitical impacts, and the situation

1.4 Data Sources

- UN and World Bank databases
- Government reports and official communications
- Industry research and academic publications

2 Literature Review

2.1 Urbanization Trends

India's urban population is vastly increasing at a quick pace, notwithstanding numerous widely accepted numbers spreading through the media that are in opposition to the recognized demographic predictions. The official information provided by the UN (World Urbanization Prospects) indicates that in the year 2025, about 44% of Indians, which is close to 589 million people, will be residing in urban areas [3]. Further estimates by the UN show that the period between 2025 and 2050, India will experience a rise of approximately 416 million new urban inhabitants, which will also be the largest growth for any other country throughout the world and will lead to almost 1.00 billion urban residents (~1005 million) by 2050. This translates into a silent yearly increase of 2% compound annual growth rate (CAGR) for India's urban population. The most important fact is that such predictions are in sharp contrast to the announcements like "India will be 60% urban by 2050," which lack backing by UN or World Bank data. On the contrary, the official forecasts reflect urbanization levels closer to ~50% or a bit below in 2050. Researchers like Bhagat (2014) point out, however, that while India's urban population will grow significantly, the country will simultaneously still have the world's largest rural population,[4] which will result in a demographic pattern that is double and thus unique to India.

2.2 VLSI and Indigenous Chips

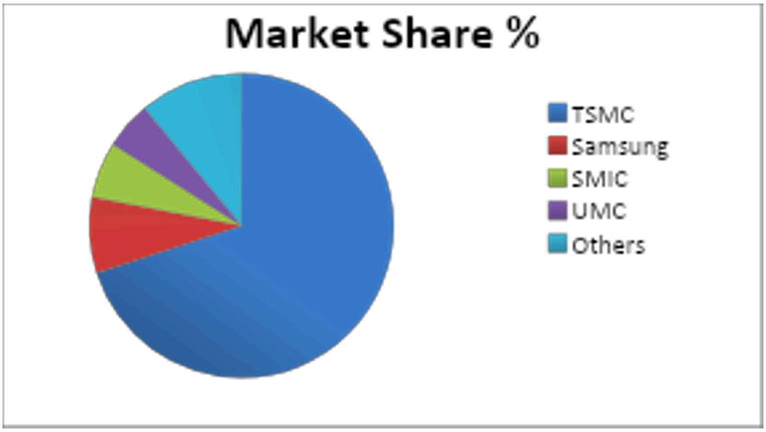

Fig.1 Global semiconductor foundry market share breakdown

Very recently, India has made its entry into the global semiconductor market, and the literature points out the advanced stage of its first indigenous microprocessor- the ISRO-designed Vikram-3201, a 32-bit chip fabricated on a 180nm process [8]. This microprocessor is suited for outer space applications, where dependability is of utmost importance, not high speed, thus rated for extreme temperatures (–55°C to +125°C) and having custom Ada-compatible instructions. On the other hand, top-tier global chips like Apple's M1/M2, Intel Core processors, AMD Ryzen, and Qualcomm Snapdragon are mass-produced in cutting-edge 7nm, 5nm, and now 3nm nodes, running on 64-bit, multi-core architectures and yielding substantially higher throughput.The advanced chip manufacturing is said to be monopolized by Taiwan's TSMC and Samsung according to literature which accounts for the joint production of 90-95% of the world's sub-7nm chips, with TSMC being the sole contributor to approximately 54% of the global foundry revenue in 2020 . In this scenario, Vikram-3201 is considered not a rival to commercial CPUs but rather a tactic showing off the country's indigenous capability where radiation tolerance and stability are being prioritized over speed. For this reason, many experts view the Vikram processor as an important step toward India's goal of achieving greater self-reliance in semiconductor technology, though that one keeps on being complementary to global chip leaders rather than competitive with them.

3 Methodology

3.1 Data Collection

UN World Urbanization data provided us with the official demographic projections to analyze India's urban growth trends. By using these datasets, we were able to calculate compound annual growth rates and to check the long-term projections (e.g. confirming that a 589M base in 2025 plus 416M projected growth results in ~1,005M urban population by 2050). For the smart city analysis, we took the data from Press Information Bureau (PIB) releases on the Smart Cities Mission and from independent assessments like Down To Earth. Additionally, we consulted industry sources to get statistics regarding hiring and semiconductor investment figures. The technical specifications of the Vikram microprocessor were sourced from reliable news websites and official reports.

3.2 Quantitative Analysis

Quantitative strategies implemented included the estimation of growth rates for the urban population in India (which was around 2% CAGR) along with a comparison of official UN/World Bank estimates with unauthenticated claims to point out differences. For the Smart Cities Mission, we measured the percentage of completed projects (93–94%) and interpreted budget utilization indicators. We also assessed the job creation in the VLSI sector in India by merging annual industry hiring trends and employment figures from the leading fabrication projects. Simple forecasting and proportional calculations were done wherever necessary

3.3 Qualitative Analysis

The qualitative components consisted of the examination of government documents, policy papers, and independent critiques to see the implementation of the project and the structural problems with it. We looked into the informative evaluations of the smart city performance, the comments made on the governance gaps, and the industry's views on the semiconductor strategies. The technical details of Vikram-3201 chip, manufacturing methods, and VLSI ecosystem challenges were studied through the lens of descriptive analysis instead of applying deep statistical modeling.

3.4 Trend and Correlation

Assessment The methodology was made up of trend analysis, for instance, the studying of the rise in the number of finished smart city projects over time along with conceptual discussions about correlations. To illustrate, we evaluated the connection between city wealth (GDP) and smart city index performance, remarking that more affluent economies tend to have the highest-ranked smart cities. Wherever feasible, data points were checked against each other (e.g. UN vs. World Bank population figures). Although comprehensive statistical regression was out of the research scope, the study still presents the important calculated metrics and interprets the significant correlations in support of the analysis.

4 Results and Analysis-

4.1 Smart Cities Case Studies + India’s Mission-

The global smart city case studies of Barcelona, Songdo, and Singapore not only show but also prove how the modern technologies like IoT, sensors, and VLSI-enabled systems can actually transform the urban management[5]. For instance, **Barcelona** optimizes traffic and energy by linking sensors to streetlights and parking; **Songdo** (South Korea) was developed as a sensor-driven city with full digital infrastructure that included waste systems running on automation as well as others, and so on. Singapore—ranked 9th globally—makes use of a national IoT network, AI-powered CCTV, a 3D digital twin called “Virtual Singapore,” and advanced traffic analytics, thus, being termed a benchmark Smart Nation. Other top-ranked cities such as Zurich, Oslo, and Geneva are also taking advantage of data platforms that are bringing about improvements in public transport, utilities, and urban livability. India to a certain extent has been inspired by these examples: Amaravati was planned using a digital twin, and several cities including Jaipur, Kochi, and Indore have implemented sensor-based public services as pilot projects. It is clear from these instances that microprocessors and VLSI-enabled devices (sensors, cameras, actuators) are the core components of smart systems. In this regard India's Smart Cities Mission has been majorly transforming urban lifestyles in 100 cities, and as of June 2025, it accomplished 94% of the 8,067 sanctioned projects (₹1.64 lakh crore) comprising the installation of e-governance platforms, water and sewerage networks, intelligent traffic systems, RFID-enabled waste tracking, and extensive CCTV setups (84,000 cameras). Today, Integrated Command-and-Control Centers are being operated in all 100 cities, where AI and real-time IoT data are being used for managing traffic, energy, safety, and services. Moreover, the mission's multi-level governance structure allows for working with international partners like the USA, Japan, and France, while central funding (₹2,400 crore in 2024–25) is there for ongoing upgrades. However, in the midst of all these constraints

4.2 IoT and Smart Infrastructure Deployment -

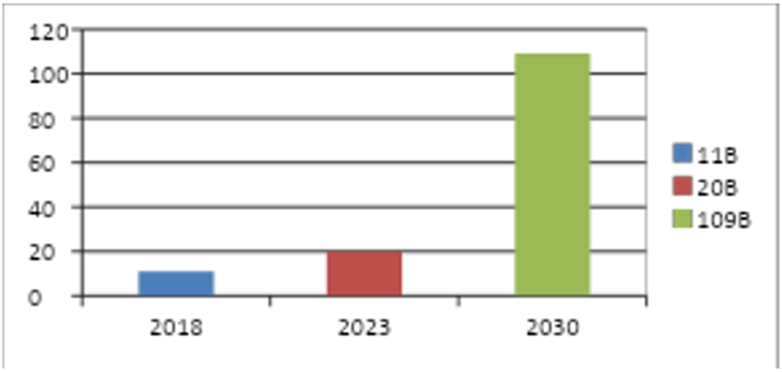

Fig. 2 Forecasted growth of the Indian IoT market from 2018 to 2030, projecting a significant revenue surge to US$109 billion.

Advanced technology through IoT (Internet of Things) devices has led to the development of smart infrastructure. The smart cities in India are already utilizing to a great extent the installation of sensors and connectivity solutions[9]. For example, the cities are installing smart meters for water and electricity, environmental sensors (for air quality and noise), traffic monitoring, and public Wi-Fi. While these technologies help cities operate more efficiently, it is equally important to ensure that citizens' data and privacy remain protected.: by the year 2025, the Indian smart cities will together produce more than 10,000 data points per second (such as traffic, power, air, etc.) to their command centers. One of the major achievements is the establishment of the national DataSmart Cities Strategy, which promotes the use of open data platforms and integrating applications. Reports from the industry show that the IoT market in India rapidly advanced.Ken Research estimates that the market was nearly US$20 billion in 2023, an increase from

US$11B in 2018 . The growth of the smart city and industrial IoT implementations partly contributed to this rise. The drivers of growth are the Digital India initiative, the expansion of broadband and 5G, and the presence of public-private partnerships. Major telecommunication and IT companies (e.g. Reliance Jio, Tata Communications, Wipro) are heavily involved in the IoT sector. We foresee that the IoT market will keep on expanding and for instance, applying the reported CAGR signifies on the scale of tens of billions by the year 2030. The projection is demonstrated in Figure 2: the market is anticipated to surge from ~US$11B in 2018 to US$20B in 2023 and ultimately reach US$109B by 2030 under current forecasts. This trend signifies the mounting investment in the infrastructure consisting of sensors, networks, and smart city platforms.

4.3 Semiconductor Technology: Vikram-3201 vs Global Chips -

Example of modern semiconductor chips and circuit boards. The Indian innovation like Vikram-3201 is entering a territory occupied mainly by the powerful global chipmakers.India's very first “Viksamyukt” CPU, the Vikram 3201, is a native 32-bit processor for satellites. Constructed at 180nm, Vikram supports 32-bit instructions and floating-point operations; however, its efficiency is far less than that of modern smartphones or PCs. According to industry reports, its design is primarily for reliability (extreme temperatures, radiation) rather than speed. On the other hand, leading global chips (for instance, Qualcomm’s Snapdragon, Apple’s M-series, and Intel/AMD CPUs) are all 64-bit multicore architectures built using 5nm/3nm processes and running at GHz frequency levels. TSMC and Samsung, for instance, account for almost all of the production of high-tech nodes (90-95% of the chips with sub-7 nm size), where the transistors are extremely small, and the Vikram’s bigger 180 nm components restrict the number of integrated circuits.

Some of the Technical comparisons that are seen through the microscope: Vikram has a unique ISA, and it is a single-core chip, whereas the global chip market consists of 64-bit ARM/x86 and very many cores. As a result, the design priorities of Vikram are quite different from those of modern consumer processors used in smartphones and personal computers. The report from NDTV/India Today says that the main goal of the Vikram 32 development is to create an Indian chip ecosystem. It is not going to pose a threat to the performance tier of commercial chips, at least not for the time being. Indeed, Indian experts worry that too much attention is being given to Vikram when considering the VLSI sector's growth more generally. The world semiconductor market is mostly ruled by Taiwan and the like, thus India's plan has been to have local fabs and design houses built simultaneously. We do not have any formal benchmarking data available for comparing Vikram with, say, an ARM Cortex-A CPU; hence, this comparison is not viable until India comes up with 64-bit designs. Therefore, our evaluation places Vikram as a landmark in the space-grade chips, but it also points to the fact that India's chip industry is still behind in the process node and ecosystem.

4.4 Global Comparisons (Taiwan, US, China) -

Smart Cities: According to estimates, China has the most smart city projects worldwide over 500 city-wide initiatives. Cities like Shenzhen, Hangzhou, and Guangzhou have taken the lead in deploying the IoT, AI, and digital governance technologies. The U.S. has a more fragmented approach: there are around 90 urban smart city projects going on in the major cities (mostly local government and tech partnerships are the driving force behind them). Singapore has always been placed at the top of world smart city rankings (for instance, #1 in IMD's Smart City Index), mainly because of its nationwide Smart Nation strategy and excellent digital infrastructure. The 100 smart cities of India are impressive in number but are not as impactful as China's smart city projects. Cities worldwide have different priorities - developed countries focus on sustainability and improving living standards (e.g. Singapore's town planning), while India prioritizes basic services and digital inclusion in its initiative.
Semiconductors: Taiwan is the largest producer of chips in the world. TSMC, its leading company, is responsible for about 68-70% of the total foundry market globally. Samsung of South Korea comes next (~8-10%) with Chinese SMIC and others sharing the rest. The U.S. is the leading country in semiconductor design and IP (companies like Intel, AMD, Qualcomm, and Nvidia), but a lot of manufacturing has been moved to Asia[6]. China has reached the point of being the largest market for semiconductor consumption in the world alone (29% of the total sales in 2023) and is putting a lot of money into building local supply chains, however, technical barriers (e.g., sanctions on the equipment) obstruct its advanced production[7]. On the other hand, India has always been an insignificant player in the global market in terms of chip production or consumption, but is promptly investing to increase its participation. So, India's path in semiconductors is at an earlier stage than Taiwan/US/China, though its integrated market (IT industry, automotives, etc.) provides a large domestic demand base.

4.5 Investments in India’s VLSI and Semiconductor Fabrication –

In the bold step to usher in the Era of semiconductor manufacturing, the Indian government is set to draw a staggering ₹76,000 crore (~$9B) in the form of incentives and support (PLI). Consequently, the Indian government has sanctioned around 10 semiconductor production projects, which are projected to cost a total of $20B, located across different states. Reports from industry insiders claim that $18 billion has been set aside for the construction of 10 new fabs. To give an example, Micron has its eyes set on a ~$3B memory packaging plant in the state of Gujarat while the TATA-Foxconn partnership aspired to a memory fab project worth around ₹1.4 lakh crore (although later modified). The fulfillment of the Tata-led ASAT (Assembly and Test) project in Assam (₹27,000 crore) alone is expected to provide job opportunities for around 25,000 to 30,000 people. Other proposed projects include wafer fabrication in Gujarat (Powerchip/Tata JV), packaging plants in Tamil Nadu (Renesas/CG Power), and display fabs in Andhra Pradesh (Jupiter).

Our assessment suggests that the majority of the support still pertains to initial phases of the projects (land acquisition, agreements). It is interesting to note that during Semicon India 2025 the minister mentioned that five fabrication "units" were being erected while 10 projects had been granted approval.Private evaluations (CRISP or investor reports) back up the noteworthy financial support of the companies like TATA, Micron, and Vedanta . On the one hand, there are \$18–20B announced in investments in a \$40B semiconductor market, that will to a great extent increase local supply by 2030 . We performed a basic financial analysis: if the cost of each fab is \$5–10B (depending on edge nodes), India is still required to build many such plants just to meet 30% of the demand. Therefore, although the investments are increasing, we must bring to attention that the market penetration through packaging/OSAT might happen before the actual front-end wafer fabs. In conclusion, while the current figures point to a massive financial inflow, they also highlight the fact that the Indian semiconductor ecosystem still requires deep pockets for continuous technological collaborations and capital.

4.6 Job Creation in India's VLSI/Semiconductor Sector-

The growth of India's semiconductor ecosystem is driving significant job creation. Industry leaders report that roughly 12,000 engineering graduates are hired annually into chip design, verification and manufacturing roles . This is up from ~8,000 a year prior, reflecting a surge in campus hiring driven by Semicon India and related schemes. Design houses (like Wipro, Tata ELXSI) and fabs source talent widely from engineering institutes, not only IITs. Compensation is also rising, with freshers now getting INR 6–12 lakhs/year, making chips competitive with IT roles.

Mega projects promise even more jobs: for example, the Tata OSAT unit in Assam (RS 27,000 cr) is expected to generate "over 25,000 direct and indirect jobs". (Some reports cite up to 30,000 jobs). Similarly, planned fabs (in Gujarat, Tamil Nadu) are projected to employ thousands in construction and operation such large-scale projects are expected to create opportunities not only for engineers but also for technicians, researchers, and support staff. (though final figures depend on full implementation). We estimate that if all current projects proceed, India could see on the order of 100,000 new semiconductor-related jobs in the next 5–10 years. These jobs span VLSI design, backend packaging, fab operations, and also indirect roles in equipment, research and services. Crucially, this is a net increase: many are new, not just reshuffled from existing IT. Thus, quantitative indicators (engineers hired, project employment) show robust growth in high-tech employment, aligning with government statements on VLSI-driven job creation.

4.7 India's Smart Cities Mission: Achievements vs. Gaps-

One of the major benefits of India's Smart Cities Mission (SCM) is that the close interaction between the state and urban populations. It was started in 2015 and aimed to bring 100 cities up to date using technology and improved infrastructure. Government data presents a very optimistic picture: at the end of the year 2020, out of the total number of 8,067 projects 7,555 (94%) were completed, along with the total investment of ₹1.64 lakh crore. There are Integrated Command and Control Centres (ICCC) in all the 100 cities which make use of AI/IoT for traffic management, utilities monitoring, etc. A number of initiatives have been completed in the areas of smart roads, healthcare centers, and open spaces, such as Cycle4Change and Streets4People . These initiatives show that smart city programs are not only focused on technology but also on improving everyday urban life for citizens. The Government has utilized almost all of the budget (99.4% of the central funds have actually been released).

Nevertheless, different independent assessments point to a situation of very serious delays and an unequal distribution of the progress[1]. By March 2025, only 18 cities had managed to finish all their projects and among the reasons for this were the lack of resources in the smaller cities, problems with acquiring land and delays caused by the need to re-tender. In addition, even though in terms of financial value ~93–94% of the projects are considered "complete", in most cases this is simply a reflection of the fact that the money has been released rather than the physical aspect being finished. The project data review we did (project data from DTE) indicates that states like Karnataka and UP have already completed >90% of their projects, but the smaller regions tend to lag behind. Furthermore, we have identified the problem of "missing policies"; for instance, the critics point out that the planning and execution guidelines (for data sharing, multi-city governance) are sometimes not clearly stated which results in inter-agency bottlenecks.

On cybersecurity, official representatives have not said anything, but CERT-In has already cautioned that the IoT-based city systems would present "a complex attack surface" that is open to being attacked by cyber threats[10]. There have been cases of smart-city projects being carried out without good security measures, thus, it is likely that there is a risk involved. If the two sets of observations are to be reconciled, the mission has not been able to deliver evenly. The data-driven analysis (project completion rates, budget spends) supports both narratives: the high level of technical adoption and investment, but also systemic delays. This balanced view aligns with a quotation in SCM press releases: technology is a tool, not a panacea.

4.8 Future Challenges to India's Semiconductor Ambitions-

India encounters several difficulties in series of obstacles in the way to future with smart effective cities and VLSI proliferation, chiefly:

Financing and Economics- The mission still needs funding despite the fact that there has been 93-94% of project completion. The WB points out that in the case of urban infra spending (2011-18) government finance covered 72%, while private finance was only

~5% . It is vital to mobilize private investment through such avenues as municipal bonds, land monetization, and PPPs. Governance and Coordination- Smart Cities projects involve a whole hierarchy of government at different levels. Ensuring coordination among national, state, and city bodies can get tricky. The local capacity (in planning, procurement, IT) is very diverse and varies a lot from one place to another. Decentralized decision-making (letting cities tailor solutions) is a double-edged sword: it makes local relevance but can slow standardization in a way. One study highlights that India's model relies on allowing cities "decide what fits best for their needs". And while this flexibility has its advantages, it also requires the existence of strong institutional frameworks (e.g., smart city SPVs, citizen forums) to prevent delays and cost overruns[2].

Technology and Standards: Rapid IoT deployment has made the issue of interoperability and security urgent. The absence of unified standards can contribute to the creation of distinct systems that do not communicate with each other. A professional report points out that "standards-based management for devices" and open APIs are the cornerstones for economical smart solutions. The Bureau of Indian Standards and industry organizations in India should. collaborate to set standards for interoperability in smart urban areas (e.g., common policies for lighting, transportation). Another area of concern is cybersecurity: the integration of critical infrastructure (power, transport) with IT systems poses a greater risk. The new data protection law (DPDP Act 2023) is a move in the right direction to secure citizen data, but it will still be a difficult task to apply security best practices to the thousands of city sensors.

Skilled Workforce: The development and management of smart systems necessitate professionals with specific skills (IoT engineering, data analytics, VLSI design). Although India has a sizable IT workforce, there is a shortage of professionals with expertise in semiconductor manufacturing and smart infrastructures. Human resource development, training, and R&D labs (e.g. SAMEER) must expand to sustain the ecosystem.

Technology Gaps in VLSI: The Vikram-3201 and the new fabs satisfy many needs but the latter are still not up to par with technology. Global competition will need investment in smaller chip nodes (sub-22nm), advanced packaging, and supply of semiconductors for high-growth sectors (AI chips, quantum) as a minimum. Global supply chain shifts (e.g. tech blockades) create both chances and uncertainties for India's semiconductor aspirations at the same time.

Urban Challenges: Besides technology, smart cities have to deal with pollution, climate change, and urban poor. The case in point is water meters being smart, but the problems of leakage and non-revenue water require change in people's behavior and some infrastructure fixes. This shows that technological solutions alone cannot solve urban problems without social and behavioral changes. Urban planners will have to make sure that sustainability (EV adoption, green buildings) and resilience to climate events will not be just theoretical but practical.

Planning integration is a requirement each of these challenges signals: the policy makers should synchronize the smart city initiatives with the large-scale objectives (such as public health and climate action) and also provide for flexible governance.

5 Discussion

As indicated in the above analysis, India has made remarkable progress in its smart city projects and VLSI initiatives but the path ahead is still very long. The Smart Cities Mission has reported high project completion rates (over 90%) and has also set up significant digital infrastructure (ICCCs, pan-city IoT networks). The IoT implementations in water, transport, and waste management are indicators of the trend where cities are getting equipped to manage through data-driven methods. The VLSI sector is experiencing the same trend as the government support (Semiconductor Mission, DLI) and flagship projects encourage the technology-based holy grail of sovereignty. Vikram-3201 is one such case that proves the in-house capabilities of chip design with respect to critical areas like space and defense.

However, doing so will not be that easy since the vision of the fully “smart” city with cutting-edge technology comes with structural obstacles. One of the major issues is financing — urban investment of $55B/year is a must and the participation of the private sector has to grow to this extent. Data interoperability and privacy are now issues to be considered in the wake of more and more IoT devices being installed. India is in a strong global competition in VLSI, even with ten fabs coming up; India will still be on the older-process chips whereas the leaders like TSMC would have already transitioned into the cutting edge sub-5nm territories. It will take a while to close this gap, and India may start by focusing on niche strengths (aerospace, defense, automotive chips) where the demand for fault tolerance is higher than for extreme miniaturization.

The future is already here and it comes with the possibilities as well as the problems. The global network of 5G and later on, 6G will increase the connectivity to a great extent but at the same time would need to build on the existing network of communication lines and towers, which will be a huge undertaking. The artificial intelligence and machine learning can not only be applied to city management to make it more efficient (predictive maintenance, adaptive transit) but would also call for the establishment of rules regarding data usage that would ensure the protection of people's rights. At the same time, it is important to make sure that these technologies are used responsibly and that citizens’ data is handled with proper care. India has an active role to play here based on its biggest asset, which is the availability of skilled software engineers, but the reliance on the availability of hardware (silicon and sensors) cannot be dispensed with hence, the indigenous chip making is the focus area.

Geopolitically, India’s technology self-reliance (atmanirbharta) is no longer a luxury but necessities as global supply chains continue to face disruptions. By fostering local VLSI and at the same time making its cities smarter, India is trying to create a win-win situation: domestic chip design supplies IoT hardware, and smart city projects generate the need for Indian semiconductors. Regional cooperation (for instance, knowledge sharing from Taiwan’s fabs or Germany’s urban planning expertise) can play a pivotal role in speeding up the process while preventing isolation.

Last but not least, the effectiveness of smart systems depends on the extent to which the public is involved and included. There are stories of some city dwellers who are making use of the smart services (smart traffic lights, free internet zones) but the general public's knowledge of the perks of the mission varies greatly. Regular communication with stakeholders, public dashboards, and information dissemination are some of the key strategies that will help in maintaining the grant of the momentum.

6 Conclusion

The smart cities program in India has been one of the most ambitious urban renewal projects worldwide. A decade on from the Smart Cities Mission's launch, it has propped up local urban planning and added ICT layers to core services. ICT and smart technologies are being seen as the main ways to drive urbanization in India. No wonder, India has embarked upon a grandiose semiconductor vision: the tech plants, design hubs, microchips (e.g., the much-publicized Vikram-3201 microprocessor) will not only cut down imports but also enclose electronics within the “Make-in-India” story.

Urbanization, IoT, and VLSI developments are all connected and have been thoroughly researched by this paper. The main results of these researches are:

- Continued progress is being made in India’s smart city projects with all 100 cities already integrating command centers (AI/IoT) for service management.
- The Indian IoT market is skyrocketing with revenues running up to billions of dollars and its being a major contributor in rural and industrial initiatives.
- The Indian semiconductor industry is in a position that is both critical and favorable: the government missions are bridging historical gaps while it will take time for the sector to catch up with the global leaders.
- The Vikram-3201 is an epitome of the Indian indigenous chip-making potential that is crucial to making "Atmanirbhar Bharat" in high-tech areas.
- When compared to the world, India is fast getting there but still behind giants like Taiwan, China, and the US in different criteria (foundry output, smart city index, etc.)
- Projected metropolitan expansion (estimated at over 600 million city dwellers by 2036) will not only increase demand for smarter infrastructure but will also worsen the problems of financing, governance, and technology.

To sum up, the synergy of smart city planning and VLSI development in India is a strategic measure against its changing demographic and economic conditions. India through its investment in smart city projects such as IoT networks, digital systems and chip manufacturing, has already addressed some of the challenges. The convergence of smart cities and semiconductor technologies indicates that India is not only catching up with global smart city trends but also passing on its own (like the Vikram chip) tech innovations back to the global market. There should be a consistent monitoring of the outcomes of smart city projects through research, and also the impact of India's increasing semiconductor infrastructure on the national development and the international technology supply chain.

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