Double Pulse Modulation On Current Harmonic Suppression

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Abstract: Electromagnetic interference is now a hot research topic, in order to ensure reliable operation of the system, you must suppress EMI device from generating sources of electromagnetic interference suppression is more effective EMI suppression methods, through the use of double-pulse modulation, can effectively output current harmonics suppression, so as to achieve the purpose of suppressing EMI.

Introduction

Electromagnetic interference and electromagnetic compatibility design is now more popular research topic. Today's electronic products to increase the complexity of the system, some functional modules susceptible to EMI, to ensure reliable operation of the system must be defined EMI limits. According to the test of experience, DC-DC converter on the switching frequency energy spikes often exceed limits affect reliable operation of the system, but there is a huge switching frequency of peripheral margin. Therefore, the phenomenon is very valuable.

From the source to reduce the EMI noise suppressing EMI noise source itself (rather than suppress the EMI generated), you can prevent EMI interference filter plus auxiliary circuits generated. The double-pulse modulation to meet this requirement, the double-pulse modulation with conventional single-pulse modulation compared with the output current spread spectrum can speak achieve harmonic suppression purposes.

In this paper, Buck circuit as the prototype, we compare the current ripple under single pulse modulation with double pulse modulation, by schematics, formula derivation and simulation, finally, the double-pulse modulation can be demonstrated effectively suppress the current harmonic.

Theoretical analysis

First ,we described the principle of single pulse modulation as follows. Figure 1 shows the schematic: From the principle illustrated in Figure 1 we can deduce adjusted output single pulse mode current expression:

$$i_{L}(t) = \begin{cases} i_{L_{d}} + k_{1}t; t \in [0, t_{e}] \\ i_{L_{d}} + k_{1}t_{e} - k_{2}(t - t_{e}); t \in (t_{e}, t_{e} + t_{f}] \\ \text{and} k_{1} = \frac{\Delta I_{L}}{DT_{s}}, k_{2} = \frac{\Delta I_{L}}{(1 - D)T_{s}} \end{cases}$$
(1)

$$\Delta I_L = \frac{V_S D(1-D)}{L f_S} = k_1 t_e \tag{3}$$

(2)



Figure 1 The principle schematic of single pulse modulation

In a double-pulse modulation cycle, according to the formula (3), We can get: Two pulse current rise and fall rates are the same, double-pulse modulation method is as follows.



Figure 2 The principle schematic of double pulse modulation

Double pulse modulation current expression is as follows:

$$i_{L}(t) = \begin{cases} i_{L_{0}} + k_{1}t; t \in [0, t_{a}] \\ i_{L_{0}} + k_{1}t_{a} - k_{2}(t - t_{a}); t \in (t_{a}, t_{a} + t_{b}] \\ i_{L_{0}} + k_{1}t_{a} - k_{2}t_{b} + k_{1}(t - t_{a} - t_{b}); t \in (t_{a} + t_{b}, t_{a} + t_{b} + t_{c}] \\ i_{L_{0}} + k_{1}t_{a} - k_{2}t_{b} + k_{1}t_{c} - k_{2}(t - t_{a} - t_{b} - t_{c}); t \in (t_{a} + t_{b} + t_{c}, t_{a} + t_{b} + t_{c} + t_{d}] \end{cases}$$

$$(4)$$

besides
$$t_e = t_a + t_c, t_f = t_b + t_d$$
 (5)

a single We pulse modulation expression harmonics, can get of the of the current $i_{L}(t) = \frac{a_{0}}{2} + \sum_{n=1}^{\infty} a_{n} \cos(n\Omega t) + \sum_{n=1}^{\infty} b_{n} \sin(n\Omega t)$ (6) $i_L(t) = \frac{a_0}{2} + \sum_{n=1}^{\infty} A_n \sin(n\Omega t + \varphi_n)$ (7) $A_n = \sqrt{a_n^2 + b_n^2}$, $\varphi_n = \arctan \frac{a_n}{b_n}$ (8) $\varphi_n = \arctan \frac{\cos(n\Omega t_e) - 1}{\sin(n\Omega t_e)}, \quad A_n = \frac{\Delta I_L T^2 \left| \sin(n\Omega t_e) \right|}{n^2 t_e t_f \pi^2}$ (9)

Double pulse modulated current harmonics: $\varphi_n = \arctan \frac{\cos(n\Omega t_a) + \cos[n\Omega(t_a + t_b + t_c)] - 1 - \cos[n\Omega(t_a + t_b)]}{\sin(n\Omega t_a) + \sin[n\Omega(t_a + t_b + t_c)] - \sin[n\Omega(t_a + t_b)]} (10)$

$$A_{n} = \frac{4}{T} \frac{(k_{1} + k_{2})}{n^{2} \Omega^{2}} \sqrt{\frac{\sin^{2}(n\pi D_{on_{1}}) + \sin^{2}[n\pi (D + D_{off_{1}}) + \sin^{2}(n\pi D_{off_{1}}) + \sin^{2}[n\pi (D - D_{on_{1}})] - (11)}{\sin^{2}[n\pi (D + D_{off_{1}} - D_{on_{1}})] - \sin^{2}[n\pi (D_{on_{1}} + D_{off_{1}})]}}$$

$$A_{n}^{'} = \sqrt{\frac{\sin^{2}(n\pi D_{on_{1}}) + \sin^{2}[n\pi (D + D_{off_{1}}) + \sin^{2}(n\pi D_{off_{1}}) + \sin^{2}[n\pi (D - D_{on_{1}})] - \sin^{2}[n\pi (D + D_{off_{1}} - D_{on_{1}})] - \sin^{2}[n\pi (D_{on_{1}} + D_{off_{1}})]}}$$
(12)

Simulation Analysis

In order to verify the double pulse modulation can be a current harmonics suppression, we double-pulse modulation output current harmonic simulation analysis in this paper to analyze the 5th harmonic, Buck circuit as the prototype, Circuit parameters are as follows : V_s =48V, D=0.5, f_s =50kHz, R=2.2 Ω The figure 3 is the relation

between A_{n} and D_{onl} , D_{offl} .



Figure 3 The relation between A'_n and D_{onl} , D_{off}

From the figure, we can see the fifth harmonic of the Minimax place, we spot a few Minimax simulation analysis:



Figure 4 The current harmonic of single pulse modulation

Through the simulation results in Figure 4 and Figure 5, we can see, change D_{onl} and D_{offl} can change the amplitude of the current harmonics, And the simulation results corresponding to Figure 3 minimax points. And compared with a single pulse modulation, a current harmonics significantly suppressed.





Figure 5 The current harmonic of double pulse modulation

Conclusion

In this paper, Buck circuit as the prototype, we compare the current ripple under single pulse modulation with double pulse modulation, by schematics, formula derivation and simulation, finally, the double-pulse modulation can be demonstrated effectively suppress the current harmonic.

References

- 1. [1]Q. Zhaoming, W. Xin, and L. Zhengyu, "Status of electromagnetic compatibility research in power electronics," in Proc. Int. Power Electron. Motion Control Conf.,2000,pp.46–57.
- 2. [2]R.Chen, J.D.vanWyk, S.Wang, and W.G.Odendaal, "Technologies and Characteristics of integrated EMI filters for switch mode power supplies," in Proc. IEEE Power Electron.Spec.Conf.,2004, vol.6, pp.4873–4880.
- 3. [3]R.Chen, J.D.vanWyk, S.Wang ,and W.G.Odendaal, "Planar electromagnetic integration technologies for integrated EMI filters," in Proc. IEEE IAS Annu. Meeting Ind. Appl. Conf., 2003,pp.1582–1588.
- 4. [4]L.E.LaWhite and M.F.Schlecht, "Active filters for 1MHz power circuits with strict input/output ripple requirements," in Proc. IEEE Power Electron. Spec. Conf. 1986, pp.255–263.
- 5. [5]N.K.Poon, J.C.P.Liu, C.K.Tse, and M.H.Pong, "Techniques for input ripple current cancellation: Classification and implementation," IEEE Trans.PowerElectron.,vol. 15, no. 6,pp.1144–1152,Nov. 2000.