# Research on Line Loss Calculation Methods Based on DC Power Flow Models

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**Keywords:** DC power flow; line loss; improved DC power flow; stochastic simulation **Abstract.** Two methods, including standard DC power flow model and the improved DC power flow model with iteration process are introduced in this paper for line loss calculation. A line loss calculation model is established based on these two DC power flow models. In order to investigate the accuracy of the DC models, 6 systems are tested with stochastic simulation procedure. Results show that the improved DC power flow model with iteration process preserves the linearity of standard DC power flow and it also gains faster convergence speed than AC power flow model.

#### Introduction

Line loss is an important technological and economic indicators of power enterprises, and it reflects planning, production and operation management level of power network [1]. Theoretical line loss is generally obtained by AC power flow, but it is complicated and time-consuming due to nonlinear equations and mass data.

DC power flow model possesses linear, non-complex properties which appeal to analytical and computational usage in power system. Solutions of DC model are non-iterative, simple and efficient. DC model is widely used in static security analysis [2,3], electricity market and economic dispatch [4,5,6]. And DC power flow model achieves high accuracy under certain conditions [7], while it is rarely used in line loss calculation [8,9].

DC power flow models are inherently approximate, and their accuracies are very system and case dependent [10]. Aiming at improving the accuracy, a number of methods derived from standard DC power flow model were presented. Reference [11] presents a method similar to DC power flow based on a known interface flow to correct node injections and admittance matrix. Reference [12] considers the active line loss, which is ignored in standard DC power flow model, with equivalent loads at both ends of the branch. Modified methods mentioned above gain good effect in specific conditions, while few accuracy tests about line loss calculation have been reported.

Given the above, this paper fulfils two main tasks. Firstly, it proposes a line loss calculation model based on two DC power flow models, including standard DC power flow model and the improved DC power flow model algorithm considering network loss. Secondly, it investigates the accuracy of the line loss calculation model.

# Standard DC Power Flow Model and Line Loss Calculation Model

# **Standard DC Power Flow Model**

Ignoring parallel branches of branch i - j and then AC power flow equations are

$$\begin{cases} P_{ij} = (U_i^2 - U_i U_j \cos \theta_{ij}) g_{ij} - U_i U_j b_{ij} \sin \theta_{ij} \\ Q_{ij} = -U_i U_j g_{ij} \sin \theta_{ij} - (U_i^2 - U_i U_j \cos \theta_{ij}) b_{ij} \end{cases}$$
(1)

Where  $P_{ij}$  and  $Q_{ij}$  are active power and reactive power flowing from node *i* to node *j*;  $U_i$  and  $U_j$  are voltages of node *i* and *j*;  $\theta_{ij}$  is the phase angle difference between node *i* and *j*, that is  $\theta_{ij} = \theta_i - \theta_j$ ;  $g_{ij}$  and  $b_{ij}$  are conductance and susceptance of branch i - j.

According to the characteristics that node voltage is close to the rated voltage, phase angle difference is very small between two nodes of a branch and resistance is much smaller than reactance in the same branch when power network works normally, therefore standard DC power flow assumes that  $U_i = U_i = 1$ ,  $\sin \theta_{ii} = \theta_{ii}$ ,  $\cos \theta_{ii} = 1$ ,  $g_{ii} = 0$ , Eq. 1 can be simplified as

$$\begin{cases} P_{ij} = -b_{ij}\theta_{ij} = \frac{\theta_{ij}}{x_{ij}} \\ Q_{ij} = 0 \end{cases}$$
(2)

Where  $x_{ii}$  is the reactance of branch i - j.

For power network with n+1 nodes, node N is set to be slack bus, and N = n+1. Then standard DC power flow equations are

$$\boldsymbol{P} = \boldsymbol{B}_0 \boldsymbol{\theta} \tag{3}$$

Where p and  $\theta$  are n-dimensional active power vector and n-dimensional node voltage phase angle matrix of all buses except the slack bus;  $B_0$  is an n-dimensional admittance matrix which considers branch reactance only.

Elements in  $\boldsymbol{B}_0$  are

$$\begin{cases} \boldsymbol{B}_{0}(i,i) = \sum_{j \in i, j \neq i} \frac{1}{x_{ij}} \\ \boldsymbol{B}_{0}(i,j) = -\frac{1}{x_{ij}} \end{cases}$$
(4)

## Line Loss Calculation Model Based on DC Power Flow

Branches are regarded as lossless branches in standard DC power flow model with no reactive power. In order to calculate line loss, [13] brings in scale factor  $\alpha_{ij}$  to take the ignored reactive power into account. Then, line loss calculation model based on DC power flow is as

$$P_{\text{loss},ij} = S_{ij}^2 r_{ij} = (\alpha_{ij} P_{ij})^2 r_{ij}$$
(5)

$$\alpha_{ij} = \frac{S_{ij}}{P_{ij}} = \sqrt{1 + \frac{r_{ij}^2}{x_{ij}^2}}$$
(6)

Where  $S_{ij}$  is the apparent power of branch i - j,  $r_{ij}$  is the resistance of branch i - j, and  $\alpha_{ij}$  is the scale factor between apparent power amplitude and active power of branch i - j.

Total line loss of the whole network is

$$P_{\text{loss},\Sigma} = \sum_{i-j\in\Omega} P_{\text{loss},ij}$$
(7)

Where  $P_{loss,\Sigma}$  is theoretical line loss of the whole network, and  $i - j \in \Omega$  represents all branches of the network.

#### The Improved DC Power Flow Model with Iteration Process

#### **Network Loss Equivalent Load Model**

Reference [12] introduces network loss equivalent load model into DC power flow model in order to improve accuracy of standard DC power flow. Based on the same network loss equivalent load model, reference [13] proposes a DC optimal power flow algorithm. Then this paper intends to introduce network loss equivalent load model into line loss calculation with DC power flow model.

Fig. 1 shows the network loss equivalent load model, the model takes active power loss  $P_{loss,ij}$  of branch i - j into account and that is ignored in standard DC power flow model. At the same time,  $P_{loss,ij}$  is divided equally into two parts in both ends of branch i - j.



Fig. 1 Equivalent load model of network loss

In the network loss equivalent load model, formula of branch power flow is

$$P_{ij}' = \frac{\theta_i' - \theta_j'}{x_{ij}} = \frac{\theta_{ij}'}{x_{ij}}$$
(8)

It shares the same form with standard DC power flow model described above.

### The Improved DC Power Flow Model

The improved DC power flow model with iteration process is based on network loss equivalent load model. Difference between the improved DC power flow model and standard DC power flow model is that the former one takes active power loss  $P_{\text{loss},ij}$  into account, and  $P_{\text{loss},ij}$  is used to modify node injection power. The improve DC model proceeds as following stars.

node injection power. The improve DC model proceeds as following steps.

*Step1.* Preparing basic data for standard DC power flow, including node injection power vector  $\boldsymbol{P}^{(1)} = [P_1, P_2, \cdots, P_n]^T$ , node admittance matrix  $\boldsymbol{B}_0$  and voltage phase angle vector  $\boldsymbol{\theta}^{(1)} = [\theta_1, \theta_2, \cdots, \theta_n]^T$ , set iteration times k = 1, set iteration precision  $\boldsymbol{\varepsilon} = \varepsilon_0$ , and set  $P_{\text{loss},ij}^{(0)} = 0$  for all branches;

*Step2*. Solve equations  $\mathbf{P}^{(k)} = \mathbf{B}_0 \boldsymbol{\theta}^{(k)}$  and get branch power flow of all branches, combine these with Eq. 5, then line loss  $P_{\text{loss},ij}^{(k)}$  can be calculated;

Step3. Judge convergence of current iteration, if all branches satisfy

$$\left|P_{\text{loss},ij}^{(k)} - P_{\text{loss},ij}^{(k-1)}\right| < \varepsilon \cdot P_{\text{loss},ij}^{(k-1)}$$
(9)

Then this iteration converges, iterations are completed, otherwise, move to Step4;

*Step4*. Calculate equivalent load  $P_{equ,i}^{(k)}$  and its vector  $P_{equ}^{(k)}$ :

$$P_{\text{equ},i}^{(k)} = \sum_{j \in i, j \neq i} \frac{P_{\text{loss},ij}^{(k)}}{2}$$
(10)

$$\boldsymbol{P}_{\text{equ}}^{(k)} = \left[\boldsymbol{P}_{\text{equ},1}^{(k)}, \boldsymbol{P}_{\text{equ},2}^{(k)}, \cdots, \boldsymbol{P}_{\text{equ},n}^{(k)}\right]^{\mathrm{T}}$$
(11)

Step5. Calculate the new node injection power vector

$$\boldsymbol{P}^{(k+1)} = \boldsymbol{P}^{(1)} - \boldsymbol{P}^{(k)}_{equ}$$
(12)

*Step6*. Update literation times k = k + 1, and if  $k > k_{max}$ , literation times reaches the upper limit, and it means the equations are not convergent this time and it comes to the end the run, otherwise, return to Step2.

# **Line Loss Calculation**

When the calculation process comes to the end, calculate loss of the network with Eq. 13 if the equations are convergent

$$P_{\text{loss},\Sigma} = \sum_{i-j\in\Omega} P_{\text{loss},ij}^{(k)}$$
(13)

While if the equations are not convergent, loss of the network can be calculated with branch line loss in the first iteration, that is

$$P_{\text{loss},\Sigma} = \sum_{i-j\in\Omega} P_{\text{loss},ij}^{(1)}$$
(14)

Results calculated by Eq. 14 equals that of Eq. 5 to Eq. 7 for the same network.

# Assessing DC Models Accuracy

### **Case Simulation Design**

In order to analysis the accuracy of the standard DC power flow model and the improved DC power flow model in the fields of line loss calculation, this paper designs a test procedure as Fig. 2. Six systems are tested in the paper, including WSCC-9, IEEE-30, European-89, IEEE-118, Polish-2383 and Polish-3120.

Take IEEE-30 system as an example to illustrate the test procedure in Fig. 2. Firstly, case data of IEEE-30 system are to be prepared. Secondly, set the change interval of node injection power as [-50%,50%], which means each node injection power of all 30 nodes are changing with variation from -50% to 50%. Thirdly, secondary power balance procedure should be carried out because the random variation in the second step may produce unbalanced power, and method mentioned in [14] is taken in this paper for the secondary power balance procedure. Fourthly, calculate the AC power flow, standard DC power flow and the improved DC power flow and their corresponding losses. Fifthly, analysis the relative error between AC model and DC models. Finally, repeat above steps M times.



Fig. 2 Accuracy test procedure about two DC models

#### **Error Analysis**

For the same test system, in the  $i^{\text{th}}$  simulation, results of loss calculation are expressed as  $P_{\text{lossDC1},i}$  for standard DC power flow,  $P_{\text{lossDC2},i}$  for the improved DC power flow and  $P_{\text{lossAC},i}$  for AC power flow. Then relative errors in the  $i^{\text{th}}$  simulation between DC models and AC model can be calculated with

$$e_{r1,i} = \frac{\left|P_{\text{loss}\text{DC1},i} - P_{\text{loss}\text{AC},i}\right|}{P_{\text{loss}\text{AC},i}}$$
(15)

$$e_{r2,i} = \frac{\left|P_{\text{lossDC2},i} - P_{\text{lossAC},i}\right|}{P_{\text{lossAC},i}}$$
(16)

When simulation procedure is repeated for M times, average relative errors between DC models and AC model are

$$\overline{e}_{r1} = \frac{1}{M} \sum_{i=1}^{M} \frac{\left| P_{\text{lossDC1},i} - P_{\text{lossAC},i} \right|}{P_{\text{lossAC},i}}$$
(17)

$$\overline{e}_{r2} = \frac{1}{M} \sum_{i=1}^{M} \frac{\left| P_{\text{lossDC2},i} - P_{\text{lossAC},i} \right|}{P_{\text{lossAC},i}}$$
(18)

Set number of simulation times M = 500, set the change interval as [-50%,50%] and set iteration precision  $\varepsilon = 0.0001$ , then relative errors between DC models and AC model are shown in Table 1.

System	$\overline{e}_{r1}$	$\overline{e}_{r2}$
WSCC-9	4.9%	5.4%
IEEE-30	10.9%	8.6%
European-89	2.5%	2.3%
IEEE-118	4.0%	1.0%
Polish-2383	8.2%	1.1%
Polish-3120	3.1%	6.5%

 Table 1
 Average relative error study

Average relative errors between DC models and AC model are very small in general. Max average relative error between standard DC power flow and AC power flow is 10.9% in IEEE-30 system, and that of the improved DC power flow is 8.6% in IEEE-30 system. For the tested 6 systems, 2 out of the 6 systems have gotten higher accuracy with standard DC power flow, including WSCC-9 and Polish-3120; the other 4 systems archive higher accuracy with the improved DC power flow, including IEEE-30, European-89, IEEE-118 and Polish-2383.

### **Sensitivity Analysis**

Fig. 3 gives the sensitivity analysis results about standard DC model. When the change interval ranges from [-10%,10%] to [-50%,50%], average relative errors between standard DC power flow and AC power flow have not changed much for each of the 6 systems. Fig. 4 gives the sensitivity analysis results about the improved DC model. When the change interval ranges from [-10%,10%] to [-50%,50%], average relative errors between the improved DC power flow and AC power flow have not changed much for each of the 6 systems.

Sensitivity analysis results show that the two DC models are stable and reliable when calculating line loss. Furthermore, it's easy to find out that the improved DC power flow model gain higher accuracy in majority systems.



Fig. 3 Sensitivity analysis about standard DC model



#### **Comprehensive Comparison of DC Models**

Both Standard DC power flow model and the improved DC power flow model achieve high accuracy in line loss calculation. In most cases, average relative errors of these two methods are less than 10%. Majority of the cases show that the improved DC model gains higher accuracy.

Sensitivity analysis proves that these two DC models are stable and reliable when they are used for line loss calculation. Relative errors of both methods have not any trend of increasing when the simulation interval rises.

In the aspect of computing speed, it is well known that standard DC power flow needs to solve linear equation once only, and its solving procedure is simpler and more efficient than the AC model. As for the improved DC power flow model, it takes branch line loss into account, and the solving procedure of the improved model requires several iterations to get its branch power flow, but it repeats to solve linear equations in every iteration, that is to say, the solving procedure of the improved DC model retains the simple and efficient characteristics of standard model. Furthermore, a number of cases show that the improved DC model can converge within 2-5 times iterations when the iteration precision is set as  $\varepsilon = 0.0001$ , which is less than that of AC power flow usually.

## Conclusion

Line loss calculation based on DC power flow models is researched in this paper. After analyzing the theory and characteristics of two DC models, including standard DC power flow model and the improved DC power flow model with iteration process, a line loss calculation model based on DC models is introduced in this paper.

A test procedure is designed here to investigate the accuracy of these two DC models. Five intervals ranging from [-10%,10%] to [-50%,50%] of node injection power are tested with each of the six systems. Results show that standard DC model and the improved DC model are both stable in line loss calculation, while the improved DC model achieves higher accuracy than standard DC model in most cases.

The improved DC power flow model with iteration process preserves the linearity of standard DC power flow and it also gains faster convergence speed than AC power flow model. It can be applied for line loss calculation instead of AC model in the situation that calls for simple computing process and faster calculation speed with acceptable accuracy.

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