

Dropping Simulation of Printed Circuit Board with BGA Soldering Assembly for Optical Module Application

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Abstract—In this paper, finite element analysis was carried out to simulate the response of printed circuit board (PCB) assembly that fell and collided with a rigid surface using dropping simulation. The drop process was simulated by setting the drop parameters including falling directions and falling heights. The results revealed that 180° drop had the most severe damage for BGA solder joints. It also indicated that dynamic bending of the PCB resulted from the solder stress.

Keywords—printed circuit board; ball grid array; simulation; drop

I. INTRODUCTION

Printed circuit board (PCB) exhibits high density interconnection to realize high-speed signal transmission^[1]. The reliability of PCB with gall grid array (BGA) soldering has become increasingly prominent since the popularity of all kinds of portable electronic products. One of the most important reliability problems is impact resistance according to the research^[2,3]. Once device inevitably dropped, it might damage internal electronic component which is mainly caused by electronic packaging BGA solder joint failure. Thus, the reliability performance of solder joints is required for the formation of full reliability. This paper used the finite element to perform the board-level drop simulation of BGA solder joints. This method can evaluate the stress of BGA solder joints easily and quickly on different location of PCB with BGA soldering assembly to provide guidance to obtain a highly reliable electronic product with dropping and impact resistance for optical module application.

II. SIMULATION EXPERIMENT

A. Simulation Experiment

In the finite element simulation model, PCB assembly was designed as three-layer structure including BGA substrate, solder ball array and PCB. The solder balls (SnAg0.5Cu) were arranged in a regular matrix (6×6), the center distance of adjacent solder balls was 4.5mm. In this paper, the method of homogenized material parameters was adopted to simplify PCB model^[4]. The materials in this paper were regarded as linear elastic model^[5] and it was shown in Table I.

TABLE I. MATERIAL PARAMETERS OF PCB MODEL

Material	PCB	Chip	Solder ball
Density(g/cm ³)	1.15	2.33	7.5
Elastic Modulus(Gpa)	25	131	43
Poisson's ratio	0.39	0.3	0.34
Geometric dimensions(mm)	185×102×1.5	25×25	Diameter:0.7, Height:0.48

B. Drop Parameter

Drop parameters include gravity acceleration, drop height, drop angle, and initial velocity. 7 sets of control experiments were designed to achieve the simulation shown in Table II. The ground property was rigid, the contact damping and the initial velocity were 0. There were four drop angles as shown in Figure I. Solder joints were divided into fine-grid, the PCB was divided into coarse grid, as shown in the Figure II.

TABLE II. DROPPING PARAMETERS

Group	Drop angle	Drop height (m)
1	180°	1
2	45°	1
3	90°	1
4	0°	1
5	180°	2
6	180°	5
7	180°	10

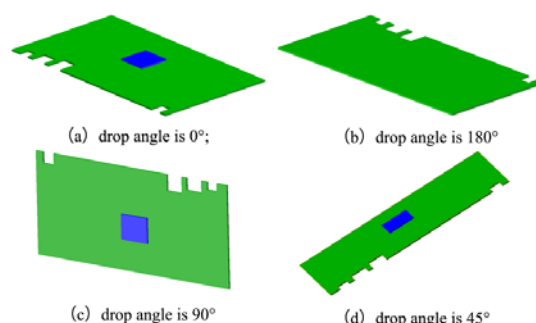


FIGURE I. DIFFERENT DROP ANGLE OF PCB

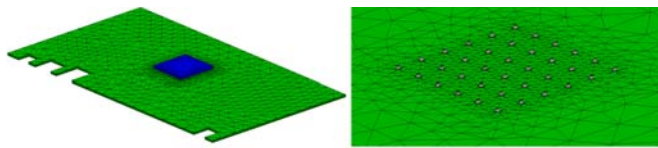


FIGURE II. FINITE ELEMENT MESH OF PCB

III. RESULTS AND DISCUSSION

A. Stress Analysis of Solder Joint Array

The result of stress distribution of the solder joint array was shown in Figure III. The maximum value of the solder joint stress in each row appeared the left and right end joints. The maximum stress value of the first row, third row, sixth row were extracted from the results, as shown in Figure IV. It displayed the maximum value of stress was located on the top and bottom two corners of the first and six column joints, and the maximum stress was almost the same as the two point. Influences of drop angle of PCB

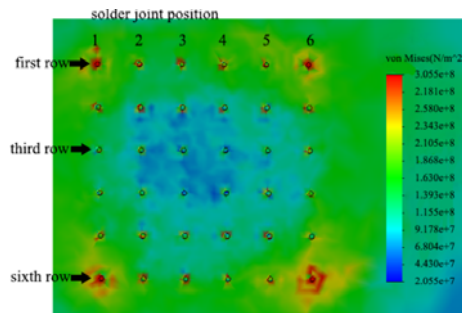


FIGURE III. STRESS DISTRIBUTION OF SOLDER JOINT ARRAY

Four kinds of typically drop angle were designed in this experiment. The distribution of stress was shown in Figure V and BGA solder joints in the first row of four groups were analyzed by drawing the maximum stress of solder joint in

Figure VI. It displayed that the maximum solder joint stress appeared at 180° drop direction and distributed in four corners of the solder joint array. Therefore the 180° drop had the most severe damage to the BGA solder joints.

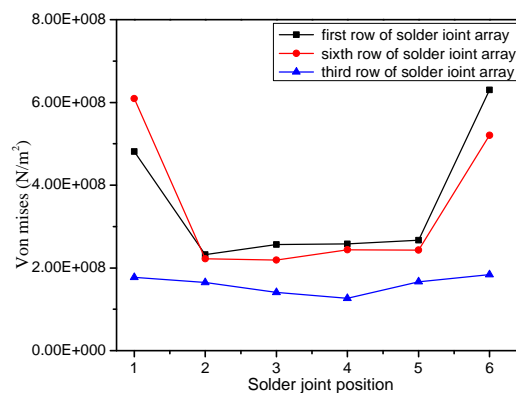


FIGURE IV. MAXIMUM VALUE OF STRESS OF SOLDER JOINT

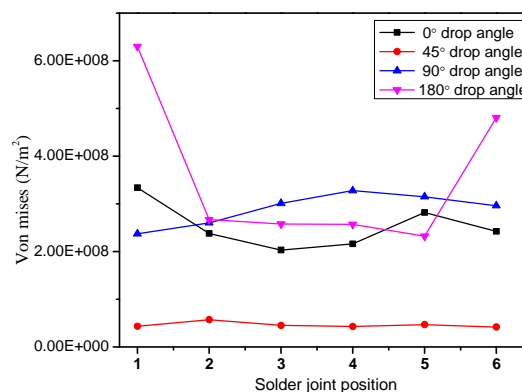


FIGURE V. STRESS OF SOLDER JOINT IN THE FIRST ROW OF ARRAY AT DIFFERENT DROP Angle

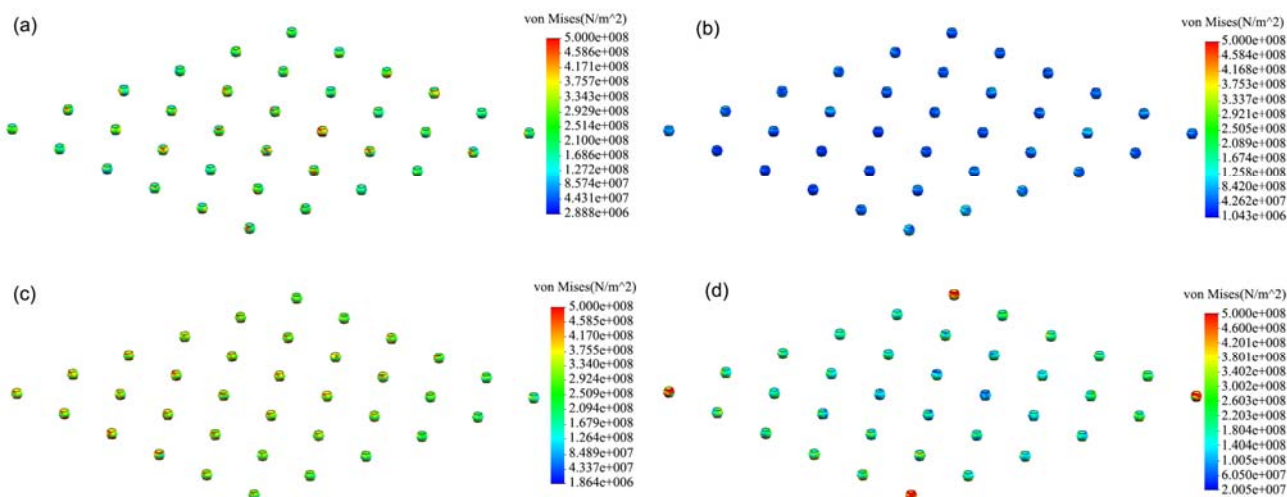


FIGURE VI. STRESS DISTRIBUTION OF THE SOLDER JOINT ARRAY AT DIFFERENT DROP ANGLE: (a) 0°; (b) 45°; (c) 90°; (d) 180°

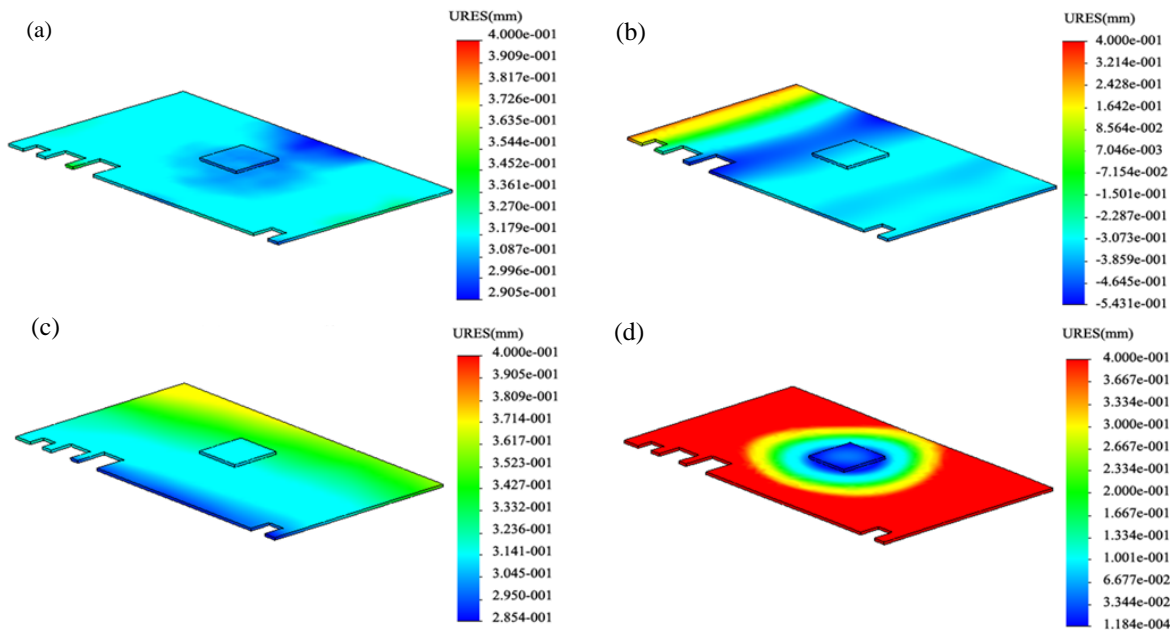


FIGURE VII. DEFORMATION OF PCB AT DIFFERENT DROP ANGLE: (a) 0°; (b) 45°; (c) 90°; (d) 180°

To study the causes of different stress of BGA solder joints while it dropped in different angle, four nephograms of deformation of PCB at different drop angle were calculated and was shown in Figure VII. It described that the 180° drop has the maximum deformation and it tended to decrease from edge to center. The maximum deformation of PCB and maximum stress of BGA solder joints from group1 to group 7 were analyzed as shown in Figure VIII. It showed strong correlations between the maximum stress and the maximum deformation. Therefore, the deformation of PCB had linear correlations with the stress of BGA solder joints and caused by the bending deformation in drop impact test.

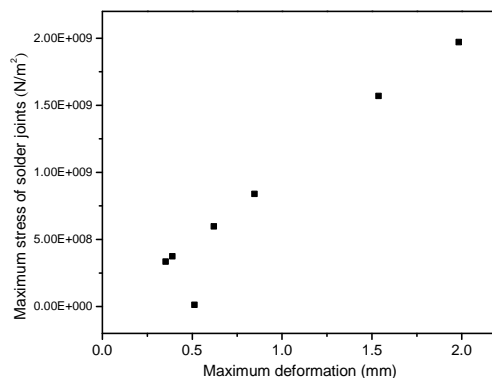


FIGURE VIII. RELATION BETWEEN THE MAXIMUM DEFORMATION OF PCB AND MAXIMUM STRESS OF BGA SOLDER JOINTS

IV. CONCLUSIONS

A novel and computationally efficient board-level model was developed to investigate board level drop reliability of

PCB assembly for optical module application. Initially, simulations of the board-level drop test were carried out using the homogenized material properties. When critical locations of the solder array were determined, it was found that peak stress occurred at the corner of solder array. It was also confirmed that 180° drop had the most severe damage to the BGA solder joints. Linear correlation was observed between the maximum deformation of PCB and maximum stress of BGA solder joints. The proposition that bending was the main driver of solder stress was validated by simulations.

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REFERENCES

- [1] Y. Chen, W. He, X. Chen, et al, "Plating uniformity of bottom-up copper pillars and patterns for IC substrates with additive-assisted electrodeposition", *Electrochimica Acta*, 120, 2014: 293-301.
- [2] S. Canumalla, H. D. Yang, P. Viswanadham, et al, "Package to Board Interconnection Shear Strength (PBISS): Effect of Surface Finish, PWB Build-up Layer and Chip Scale Package Structure", *IEEE Transactions on Components and Packaging Technologies*, 27 (1), 2004: 182-190.
- [3] J. Wang and S. Quander, "The effect of different build-up structure on solder joint fatigue life under thermo mechanical cyclic loading condition", 2004 EPTC, pp. 330-334
- [4] F. Sarvar and P. P. Conway, "Effective modeling of the reflow soldering process: Basis, construction, and operation of a process model", *IEEE Transactions on Components, Packaging, and Manufacturing Technology*, 21(2), 1998:126-133.
- [5] J. Luan, T. Y. Tee, X. Zhang, et al, "Solder joint failure modes, mechanisms, and life prediction models of IC packages under board level drop impact", 6th International Conference on Electronic Packaging Technology, 2005:382-388.