

An Adaptive Current Quick Break Protection Based on Power Frequency Variation

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Abstract. A mathematical model of resistance type superconducting fault current limiter (R-SFCL) is established and the influence to power frequency variation distance relay has been analyzed firstly. Then, it indicates that the input of the superconducting current limiter changes the circuit model before and after the fault, so that the power frequency variation impedance relay based on the pre fault memory data does not satisfy the superposition condition. An adaptive current quick break protection considering the impedance characteristics of superconducting current limiter is proposed, after the similarity and difference in principle and performance of the adaptive current quick break protection and power frequency variation impedance relay are discussed. Finally, the feasibility and effectiveness of the new method is verified by simulation in EMTDC/PSCAD.

1. Introduction

In recent years, with the development of high voltage and extra high voltage power grid and the expansion of power system scale, the phenomenon of short-circuit current exceeding standard becomes more and more serious, which brings great challenges to the security and stability of power system. At the same time, with the maturity of superconducting technology, superconducting fault current limiter has attracted worldwide attention due to its simple structure, detection / conversion / current limiting, multifunctional integration, fast current limiting speed and so on. [1, 2].

The operation of the superconducting fault current limiter has brought challenges to the existing relay protection, especially to the correct operation of the distance protection.

As a power frequency variation impedance relay, the working voltage is calculated according to the power frequency sudden change voltage and current before and after the fault, and the rated voltage of the line or the normal voltage of the fault point is used as the braking voltage. Impedance calculation is not necessary, so it has a very high speed, while it has the directivity of impedance circle, does not respond to vibration, and has strong resistance to transition resistance, so it has been widely used [3]. However, the input of a fault current limiting device represented by a resistive superconducting fault current limiter is bound to change the line parameters, which brings new challenges to the protection based on the power frequency variation of voltage and current before and after the fault.

The mathematical model of the superconducting fault current limiter has been studied in the literature [4~6]. The superconducting fault current limiter can transition to a stable high impedance mode in a few milliseconds. Therefore, based on the setting method of the fixed current impedance of the superconducting fault current limiter, it can also meet the requirements of most of the relay protection. However, the existing power frequency impedance relay the fastest export time can reach $3\sim5$ in milliseconds, whether the installment of R-SFCL will affect the power frequency impedance relay export time, need to be studied. The [7] based on the analysis of power frequency variation impedance relay mechanism and movement characteristics, pointed out that the power frequency



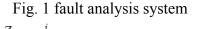
variation impedance relay is essentially a special speed adaptive current protection, so the operating characteristic, the ability of transition resistance, the influence of the running state of the system is very similar to the adaptive current protection.

It must be noted that with the SFCL started on, the line parameters changed, which lead that electrical measurements before and after fault does not correspond to the same circuit model, resulting in power frequency variation protection based on memory element losing the superposition condition. This brings in problems difficult enough to subvert the basic principle of the power frequency variation protection, while the research on this aspect is still blank. On this basis, an adaptive current fast break protection scheme based on power frequency variation is proposed in this paper, which can well adapt to the current limiting impedance characteristics, and its speed and selectivity are no worse than power frequency variation distance relay. What's more, it has better capability of transition resistance, and provides a fast and reliable protection scheme for power systems installing current limiter.

2. Working Principle

When the power system fails, it can be decomposed into the superposition of the normal operation network and the fault network [8]. As shown in Fig. 1, the voltage and current state at fault can be decomposed into the state before the fault and the state of the fault component after fault.





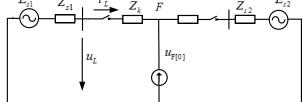


Fig. 2 equivalent circuit of power system before fault

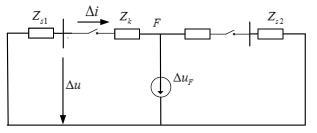


Fig. 3 equivalent circuit of fault component

In any mode of operation or operation, when the system fails, the voltage and current in the protection position meet the following formula:

$$\begin{cases} i_m = i_L + \Delta i \\ u_m = u_L + \Delta u \end{cases}$$
(1)

The physical meaning of the working voltage of an impedance relay is the change of the voltage at the end of the protection area before and after the fault. When the metal fault occurs, the voltage at fault is 0. So the physical meaning is the voltage before the short circuit. Operation principle of impedance relay based on power frequency variation is as follow:

$$|\Delta \dot{U}_{op}| = |-\Delta \dot{I}_m (Z_{s1} + Z_{set})| \ge |\Delta \dot{U}_F| = U_{F|0|}$$

$$\tag{2}$$



The voltage of the short-circuit point is not very different from the rated voltage before the fault, and the rated voltage can simplify the calculation process, so the criterion of operation is usually written as:

$$|\Delta \dot{U}_{op}| = |-\Delta \dot{I}_m(Z_{s1} + Z_{set})| \ge k_{rel} U_N$$
(3)

K is the coefficient of reliability, with a range of 1.0~1.1.

3. Impedance Characteristic of R-SFCL

The impedance characteristics of R-SFCL can be approximated by a formula.

$$R = \begin{cases} 0 & t < t_0 \\ R_{sc} (1 - e^{-t/T_{sc}}) & t_0 < t < t_0 + T_{sc} \\ R_{sc} & t > t_0 + T_{sc} \end{cases}$$
(4)

Wherein, time t_0 starts SFCL, t is the time after the failure occurs, and the T_{sc} indicates the transition time from the RSFCL started to the stable resistance. When the superconducting fault current limiter is started, the resistance rises rapidly. R-SFCL quench at 1s, resistance characteristics shown in figure 4.

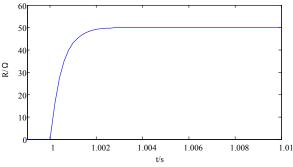


Figure 4 R-SFCL resistance characteristics

4. Influence of R-SFCL on Power Frequency Variation Impedance Relay



Fig. 5 double terminal power supply model

As shown in Fig. 5, the load current and voltage before the fault are respectively $i_{|0|}$, $u_{|0|}$, and the load current and voltage after the start of R-SFCL are respectively i_L , u_L .

$$\dot{I}_{|0|} = \frac{\dot{E}_1 - \dot{E}_2}{Z_{s1} + Z_L + Z_{s2}}$$

$$\dot{I}_{-} = \frac{\dot{E}_1 - \dot{E}_2}{(5)}$$
(5)

$$I_L = \frac{1}{Z_{s1} + Z_L + Z_{s2} + R_{fcl}}$$
(6)
In the setting process of power frequency variable impedance relay, the fault component is

In the setting process of power frequency variable impedance relay, the fault component is subtracted from the current and voltage measured after the fault and the load current and voltage of the 1~3 cycles before the fault.

The formula is as follows:

$$\begin{cases} \Delta i_{*} = i_{km} - i_{|0|} \\ \Delta u_{*} = u_{km} - u_{|0|} \end{cases}$$
(7)



Combining formula $(1),(3) \sim (7)$,

the power frequency variation before R-SFCL started is the same as the fault component; the power frequency variation after R-SFCL started includes not only the fault component, also includes the influence of current limiting resistance; frequency variable impedance relay based on the conditions of application of superposition lose application condition.

Before R-SFCL started:

$$\begin{cases} \dot{U}_{|0|} = \dot{E}_{s1} - \dot{I}_{|0|} Z_{s} \\ \dot{U}_{F|0|} = \dot{U}_{|0|} - \dot{I}_{|0|} Z_{k} \end{cases}$$
(8)
After R-SFCL started:

$$\begin{cases} \dot{U}_{L} = \dot{E}_{s1} - \dot{I}_{L} Z_{s} \\ \dot{U}_{FR[0]} = \dot{U}_{L} - \dot{I}_{L} (Z_{k} + R_{fcl}) \end{cases}$$
(9)

According to the formula (3) ~ (9), the Δi_* calculation value based on memory data is too small, which leads to the smaller setting value of the work voltage $\Delta \dot{U}_{op*}$. The movements are shown in figures 6 and 7.

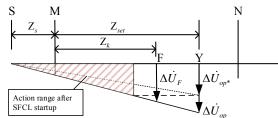


Fig. 6 ext(10)ernal directional fault voltage after R-SFCL started

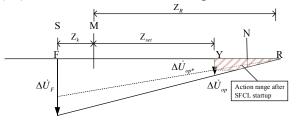


Fig. 7 reverse directional fault voltage after R-SFCL started

Same fault occurs in the same position, after SCFCL started, the working voltage $\Delta \dot{U}_{op^*}$ calculated by power frequency variation based on memory data is smaller, if still using the original brake voltage, will lead to internal fault protection area is decreased or even completely out of action, the external fault protection provided high selectivity.

5. Principle of Adaptive Current Fast Breaking Protection With RSFCL

When there is no superconducting fault current limiter, the power frequency variation impedance relay criterion (3) can be rewritten as:

$$|\dot{I}_{k} - \dot{I}_{L}| \ge \frac{k_{rel} U_{N}}{|Z_{\Delta} + Z_{set}|}$$

$$\tag{10}$$

Where $Z_{\Delta} = -\frac{\Delta U}{\Delta i}$, when external directional fault occurs $Z_{\Delta} = Z_{s1}$, when reverse directional fault

occurs $Z_{\Delta} = -Z_R$.

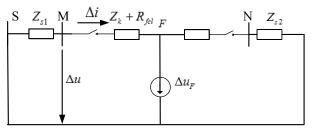
The operation criterion of adaptive current quick breaking protection is:

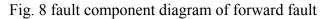
$$|\dot{I}_{k}| \ge \frac{k_{rel} U_{N}}{|Z_{\Delta} + Z_{set}|}$$

$$\tag{11}$$



When the superconducting fault current limiter is installed, when positive direction fault occurs shown as Figure 8, from the terminal power S to the relay installation position M, the circuit model before and after the fault has not changed, so the use condition of the superposition theorem is still satisfied.





Deduce by Formula (7): $\Delta \dot{U}_{*} = -\Delta \dot{I}_{*} Z_{s1}$ (12) $F = \frac{Z_{k}}{\Delta u} = \frac{Z_{R}}{\Delta u}$

Fig. 9 fault component diagram of reverse fault

In reverse fault, $\Delta \dot{U} = \Delta \dot{I}Z_R$, The load current $\dot{I}_{|0|}$ recorded by memory element is bigger than real value, on the contrary The load voltage $\dot{U}_{|0|}$ recorded by memory element is smaller than real value.

$$Z_{\Delta^*} = -\frac{\Delta \dot{U}_*}{\Delta \dot{I}_*}, \quad Z_{\Delta^*} < 0, |Z_{\Delta^*}| < Z_R$$
(13)

Deduce by Formula (10)~Error! The reference source was not found. action criterion of forward fault is as follow:

$$|\dot{I}_{k}| \geq \frac{k_{rel} \cup_{N}}{|Z_{\Delta^{*}} + Z_{set} + R_{fcl}|}$$

$$\tag{14}$$

Deduce by Formula $(5) \sim (9)$, in reverse fault:

$$|\Delta \dot{I}(Z_R - Z_{set} - R_{fcl})| = |\dot{U}_{FR[0]}| \le k_{rel} U_N$$

$$\tag{15}$$

Combining the formula (14), (15), an adaptive current fast tripping protection action criterion suiting R-SFCL is as follow:

$$|\dot{I}_{k}| \ge \frac{k_{rel} \mathbf{U}_{N}}{|Z_{\Delta^{*}} + Z_{set} + R_{fcl}|}$$

$$\tag{16}$$

6. Simulation Verification

Build the mathematical model of R-SFCL on the PSCAD and establish a two terminal power supply model, as shown in figure 5. The voltage at the sending end is 230kV, and the voltage at the receiving end is 220kV. The power impedance is: $r = 0.01\Omega$, $x = 5.84\Omega$; $L_{AB} = 100km$, $L_{BC} = 80km$, the line parameter is $r = 0.08\Omega/km$, $x = 0.417\Omega/km$, when the R-SFCL started $R_{fcl} = 30\Omega$.



7. Characteristics Analysis of Power Frequency Variation Impedance Relay

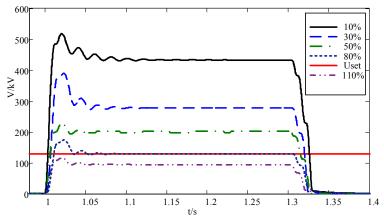


Fig 10 Action characteristics of power frequency variation impedance relay Metal grounding fault occurs at 1s. The working voltage is shown in Figure 10. The protection range is 80% of the line, and the detailed action conditions are shown in Table 1. No fault outside the zone. It has good selectivity. R-SFCL reduced the range of motion in the back zone to 32%. Table 1 Action characteristics of power frequency variation impedance relay before and after

R-SFCL started						
	act correctly(Y/N)					
location	-5%	10%	30%	32%	80%	90%
NoSFCL	N	Y	Y	Y	Y	Ν
SFCL	N	Y	Y	N	Ν	Ν

8. Performance Verification of Adaptive Current Protection Operation

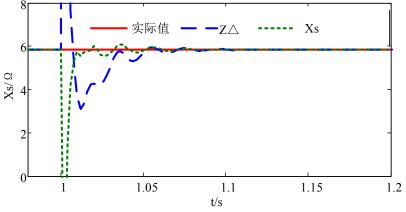


Fig 11 Additional impedance and equivalent internal resistance in internal faul

When the A phase occurs at the position of line 10%, a metal grounding fault occurs in the 1s phase. When the positive direction fault occurs, the calculated equivalent impedance and the actual impedance of the power supply are shown in figure 11. After 20ms, the resistance value is calculated around the actual value. After 110ms, the equivalent internal resistance calculation is stable. Within a cycle with the error table 2 after fault, the results are not accurate, this is because the the time window of Fourier transform is about half a cycle to one cycle, the calculation results can accurately represent the system internal resistance only after all data in the window are the faulted data. Therefore, subject to the Fourier time window length, there is transient time during which the calculated value is inaccurate, one cycle later the error is less than 5.6%, error between calculated additional impedance and real system resistance is smaller, the reliability of adaptive instantaneous current protection is quite high.

Actual equivalent impedance of power supply Xs=5.384Ω					
time/s	CALC value	err%	time/s	CALC value	err%
1.00	5.532	-5.285	1.06	5.841	0.009
1.01	5.672	-2.888	1.07	5.842	0.024
1.02	5.838	-0.035	1.08	5.843	0.041
1.03	5.808	-0.562	1.09	5.843	0.041
1.04	5.830	-0.176	1.1	5.843	0.045
1.05	5.836	-0.074	1.11	5.843	0.044

 Table 2 Calculation error analysis of equivalent impedance of power supply

 Actual equivalent impedance of power supply Xs=5 3840

T 11 0	· •		C 1	, ,·
Table 4	action	range	of relay	nrotection
I doit J	action	range	UI ICIAY	protection

locati	on	-5%	10%	30%	32%	80%	90%
No SFCL	Iset1	N	Y	Y	Y	Y	N
SFCL	I _{set1}	N	Y	Y	N	Ν	N
SFCL	Iset2	N	Y	Y	Y	Y	N

 I_{set1} is the original adaptive current setting threshold with current limiting resistance uncompensated, and the I_{set2} is the current threshold after compensation. After the current limiter is put into operation, the original protection range is obviously reduced. After the compensation, the adaptive current quick tripping protection can effectively restore the protection range.

	Maximum transition resistance $/\Omega$			
	Witho	FCL		
location	impedance relay	Adaptive current protectio		
1%	30.9	39.5	19	
5%	30.5	39	19	
10%	30	38.5	18.5	
20%	27	35	16.5	
30%	22	30	13	
50%	12	18	6	
70%	5	8	3	
80%	0.5	3	0.5	

Table 4 Fault position and the maximum transition resistance

Table 4 shows that the closer the fault location is to the protection and installation position, the stronger the transition resistance capability is. The adaptive current quick breaking protection has the ability of transition resistance, which is stronger than that of the power frequency sudden change impedance relay. After the fault current limiter is put into operation, the transition resistance capability of the adaptive current fast breaking protection is still obviously reduced, even if it has been compensated.

Table 5 Fault location and action sequence

ruore o ruure robution und action sequence					
time/ms					
	impedance Adaptive current				
location	relay	protection			
	With out FCL		FCL		
1%	2.50	3.51	3.61		
10%	2.63	3.65	3.51		
30%	2.67	2.48	2.76		
50%	6.13	4.16	3.81		
70%	7.98	4.21	3.81		
80%	8.91	5.40	3.89		



Table 5 shows that the power frequency variable impedance relay has a good quick action, and the closer the fault location is to the protection installation position, the faster it is. The average operation time of the adaptive current quick breaking protection is no longer than that of the power frequency variation impedance relay, and the action time is hardly affected by the fault position.

9. Conclusion

1) The start-up of SFCL changes the circuit parameters, the power frequency variation includes not only the fault component, but also the influence of the current limiting resistor, Power frequency variation impedance relay based on the superposition theorem is not satisfied superposition condition.

2) This paper puts forward an adaptive current quick break protection based on power frequency variation. Research shows that the protection principle have a speed no slower than the power frequency variation impedance relay, and it has a stronger ability of transition resistance, what's more, its action time can not be affected by fault location. It provides a new protection solution for the power systems with current limiting equipment installed.

Acknowledgements

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