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Performance Analysis of Two-Stage Signal Processing Structure in Antenna Array

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Abstract. Computation afford of adaptive antenna technology is the key factor limited its applications. This paper presented a new two-stage structure to solve this problem. By dividing array antennas into two group, two–stage structure can reduce computation afford with the same system performance of one-stage structure as the traditional adaptive antenna technology does. Algorithm and criterion in each stage can be selected independently according to the applications situations. Theoretical analyses of system performance in two-stage and one-stage structures are presented, and computation simulations are performed using RLS algorithm. Due to the low-complexity and low-computation, more antennas can be used to improve performance. For diversity receiving, it can help to improve SNR and less computation afford is obtained with the same performance as the traditional processing structure. The presented two-stage structure can be generalized easily to multiple–stage structure.

Introduction

In wireless communications, how to remove the influence from co-channel interference (CCI), multi-address interference (MAI) and multi-path fading has become the key aspect when system performance is analyzed. Adaptive antenna technology employs digital signal processing technologies to improve SINR through adjusting beam direction of antenna array.

Because there are more users and real time need in adaptive antenna system, the ability of rapid computation is necessary for adaptive antenna system. In fact, digital processing technologies have been the key factor that limits the applications of adaptive antenna system. Until now, a lot of documents have adopted antenna diversity to solve the computation afford. After processed by antenna diversities, such as selection combining, equal-gain combing and maximal-ration combining, array signals can be processed with adaptive algorithms (for example MLSE and CMA).

System Model

The presented algorithm is shown in Fig.1. Digital signal processing can be performed in base band or mid-frequency band as usually. Two stage processing algorithm, however, is employing. Through A/D transformation, signals on the array elements are divided into two groups, with signals on each group being processed independently and being output. The output signals on each group of antennas are input to process in DSP, general CPU or ASIC/FPGA.

With processing signals into two-stage, we can select different algorithms, even different criterions, according to applications needs and signals properties and signals quality and convergence speed are to be improved. At the same time, due to dividing, the number of antennas in each group is less, and the computation afford will be reduced. If RLS algorithm is used, computation afford is proportionate to square of array elements' number.



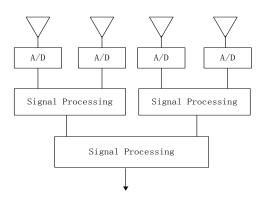


Figure. 1. The structure of the two-stage processing

Performance Analysis

We assume there are *N* antennas and *M* transmitting sources. The base band signal on the *n* th array element at $t = kT_0$ can be written as [2]:

$$x_n(k) = \sum_{m=1}^{M} (h_m^n)^T a_m(k) e^{jk_c(n-1)l\sin\theta_m} + n_n(k)$$
(1)

where $a_m(k)$ is the vector according to the *m* th source, h_m^n is the channel impulse response corresponding to the *m* th source signal and the n^{th} array element, θ_m is the DOA of the *m* th source signal, *l* is the inter-element distance, k_c is the wave number, $n_n(k)$ is noise. If the adaptive algorithm is based on Weina solution and training sequence is used, the processed signal is the strongest one and the other signals are considered interference signals. When the number of interference signals is big enough, white Gauss noise is assumed in CDMA system. For simplicity, we assume only one signal and the powers between signal and noises are no different. Therefore, (1) can be simplified as:

$$x_n(k) = h_n s(t) e^{i(n-1)\beta_d} + n_n(k)$$
(2)

where s(t) is the transmitted base band signal, β_d is the phase difference between signals on different array elements.

The received signals on the antenna array can be written as:

$$X(k) = S(k) + N(k) \tag{3}$$

where,

$$S(k) = [s(k)h_1, s(k)h_2 e^{i\beta_d}, \dots, s(k)h_n e^{i(N-1)\beta_d}]^T$$

= $s(k)\Gamma^T = [S_1(k), S_2(k), \dots, S_N(k)]^T$ (4)

$$N(k) = [n_1(k), n_2(k), \cdots, n_N(k)]^T$$
(5)

Due to stable signals and Gauss white noise, we can assume:

 $E[S_i^*S_i] = P_s \tag{6}$

$$E[n_i^*n_i] = P_n \tag{7}$$

where, * denotes conjugate.

From Wiena theorem, we can get [3]:

$$w(k) = R_x(k)^{-1} R_{xX}(k)$$
(8)



where,

$$R_{x}(k) = E[X(k)^{*}X(k)^{T}]$$
(9)

$$R_{xX}(k) = E[x(k)X(k)^*]$$
⁽¹⁰⁾

To define:

$$\rho = \frac{P_s}{P_n} \tag{11}$$

Due to irrelevance among noises and between noises and the received signals, we can get:

$$R_{x}(k) = P_{n}(I + \rho \Gamma^{H} \Gamma)$$
(12)

$$R_{xX}(k) = P_s \Gamma^H \tag{13}$$

where H denotes conjugate transpose. According to reverse matrix solution, we can get:

$$R_{X}^{-1}(k) = P_{nl}^{-1} [I - \frac{\rho \Gamma^{H} \Gamma}{1 + \rho M}]$$
(14)

$$w(k) = \frac{\rho}{1 + \rho M} \Gamma^{H}$$
(15)

The output signals can be expressed as:

$$\hat{s}(k) = w(k)^{H} X(k)$$

$$= \frac{\rho N}{1 + \rho N} s(k) +$$

$$\frac{\rho N}{1 + \rho N} [n_{1}(k) + n_{2}(k)e^{j\beta_{d}} + \dots + n_{N}e^{j(N-1)\beta_{d}}]$$
(16)

This signal isn't the same as ideal signal, and it can be the signal affected by noised. Expression (16) include two items, with former being signal and latter being noise. We define the processed power of noise, which is different with [4]:

$$P_n = E\left[\left|\hat{s}(k) - \frac{\rho N}{1 + \rho N} s(k)\right|^2\right]$$
(17)

The power of the interference and noise can be expressed as:

$$P_n^{1}(k) = E\left[\left|\hat{s}(k) - \frac{\rho N}{1 + \rho N} s(k)\right|^2\right] = \frac{\rho^2 N}{\left(1 + \rho N\right)^2} P_n$$
(18)

SINR of the received signals after signal processed is: $\rho_1 = N\rho$

Now we analyses the system performance of two-stage signal processing structure.

Assuming there are MN antennas and each group has N antennas with M groups in the first stage. In the second stage, M output signals are processed, and the coefficients are:

$$w_{1}(k) = \frac{\rho_{1}}{1 + \rho_{1}M} \Gamma_{1}^{H}$$
(20)

The optimal filtered signals are expressed as: $\hat{s}(k) = w_1(k)\hat{s}(k)$ (19)

(21)



Because the input noises are irrelative in the second stage and noises and signals are irrelative, the processed power of interferences and noises is:

$$P_n^2 = \frac{\rho_1^2 M}{\left(1 + \rho_1 M\right)^2} P_n^1 = \frac{\rho_1^2 M}{\left(1 + \rho_1 M\right)^2} \frac{\rho^2 N}{\left(1 + \rho N\right)^2} P_n$$
(22)

The processed power of the signals is:

$$P_{\hat{s}} = \frac{\rho_1^2 M^2 \rho^2 N^2}{(1 + \rho_1 M)^2 (1 + \rho N)^2} P_s$$
(23)

It is easily noted that SNR is $MN\rho$ after processed by two- stage processing structure. If expression (19) is used directly, the same result is also obtained. It should be noted that SNR is also $MN\rho$ if no two-stage structure is used. Therefore, the system performance between two-stage and one-stage has no theoretical difference.

Form the point of computation afford, two-stage structure has low-computation. This point can be explained with RLS as the example. In general, the computation complex is proportionate with the quantity of $(MN)^2$; however, the computation complex is relative to the quantity of $(M \times N^2 + M^2)$ in two-stage structure. For example, if we use 16 array elements, the computation afford of two-stage structure is $\frac{5}{16}$ as

that of one-stage structure.

Simulations

Simulations are performed in MATLAB, and the flow structure is shown in Figure.2.

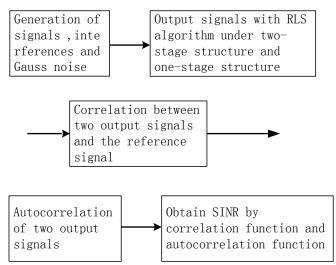


Figure. 2. The computer simulation flow diagram

The length of the reference sequence is 5000, and BPSK is used. Figure. 3 shows the results under array element's number ranging from 2 to 16, and no interference signals are taken into account with noise power being 0dB



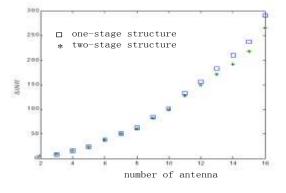


Figure. 3. The relation between SINR improved multiples and number of antenna

In Figure. 4, there are 25 array elements in one-stage structure and 5 antennas in the first stage for two-stage structure. The results are obtained under interference number arranging 1 to 40. The amplitude of signal is 1, while amplitude of each interference signal is 1/20 and noise 1.

From Figure. 3, Figure.4 and Figure.5, we can that system performance under one-stage structure and two-stage structure is similar.

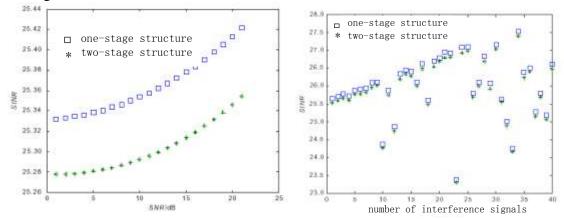


Figure. 4. Improved multiples of SINR under different input SNR Figure. 5. Improved multiples of SINR under different number of interference signals

Conclusions

In this paper, we presented a two-stage structure to process signals, and the performance analysis is also done. Due to the low-complexity and low-computation, more antennas can be used to improve performance. For diversity receiving, it can help to improve SNR and less computation afford is obtained with the same performance as the traditional processing structure. Owing to the characteristic of two-stage structure, algorithm and criterion can be selected according to the real applications situations. The presented two-stage structure can be generalized easily to multiple –stage structure

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