Design of Dual-mode RFID Reader

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Abstract—The Internet of Things accelerates the development of RFID technology. Logistics, transportation, aviation and other fields has increasingly high demand on the reader's identification distance and reliability. In order to meet the market demand, the design of RFID reader based on the dual-mode was proposed. One is 915MHz radio frequency identification mode. The other is 2.4GHz active radio frequency identification mode, which has lower transmission power requirement and farther valid identification distance. An ARM9 microcontroller is used as the main controller of the RFID reader. The design includes communicate with PC, control dual-mode RFID reader, control the reader to write and read tags and the study of anti-collision algorithm. In the testing of the system, it achieved to identify further. The performance is achieved and reliable.

Keywords-RFID; reader; dual-mode; the Internet of things

I. INTRODUCTION

Internet of Things has recently become a research focus. As the key technology, RFID (Radio Frequency Identification) is a non-contact automatic identification technology that uses radio-frequency electromagnetic fields to transfer data. Because of its characteristics include long read-distance, quick speed, strong permeability and bearing the environment, it is widely used in many fields, such as transportation, logistics, aviation, identification and manufacturing[1], etc. Divided in accordance with their radio frequency, there are low-frequency (125 or 134.2 kHz), high frequency (13.56 MHz), UHF (868 ~ 956 MHz) and microwave (2.4GHz) in the RFID products^[2]. UHF (915MHz) is achieved mainly through the capacitive coupling. It can manner to achieve a good reading distance, but is difficult to define the reading area. Active RFID technology (2.4GHz) has the characteristics of low transmit power, long distance communication, good compatibility and high reliability. Compared with passive RFID, the advantage is very obvious. Complicated as transportation, logistics network, the demand for dual-mode radio frequency technology is urgent. It has broad market prospects.

II. SYSTEM GENERAL STRUCTURE

In the design of the dual-mode RFID reader, embedded system design concepts and methods were used. The system is made up of hardware and software. The hardware is the platform that achieves system operation, including a processor and various peripheral interfaces; software controls the system operation, including hardware driver, operating system and various applications. ARM9-based controller can complete the reading and writing of the 915MHz and 2.4GHz radio frequency (RF) signal, and the system general diagram is shown in Figure 1.

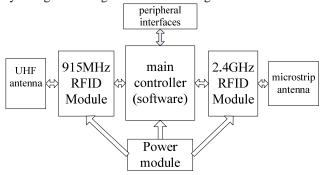


Figure 1. The system general diagram

UHF antenna and the microstrip antenna respectively receive the 915MHz and 2.4GHz electromagnetic waves and communicate with the tags. The normal work of the main controller and RF module was provided energy by power module. The 915MHz RFID modules and 2.4GHz RFID modules complete RF signal modulation and demodulation, coding and decoding, signal amplification and the transmission and reception, etc. Main controller equipped with the operating system controls the RF signal to read and write. The reader transmits energy through the antenna and receives the return data; Then the received data is sent to the controller for processing; Finally, complete control and read-write operation of the dual mode RF signal.

III. SYSTEM HARDWARE DESIGN

A. 915MHz RFID module

The design selected RF transceiver chip AS3992 developed by the ams. Integrated with UHF RFID RF frontend and the ISO/I EC18000-6C protocol processor, including a series of advanced features, such as programmable DRM filters, pre-distortion function and the -86dBm ultra-high receive sensitivity [3], the device is suitable for a wide range of UHF RFID applications. Parallel or serial interface can be selected for communication between the MCU and the reader.

The 915MHz reader module can be divided into a transmitter circuit, signal isolation, and the reception circuit, wherein the transmitting circuit was mainly composed of

AS3992 output matching circuit, the power amplification circuit and a filter circuit. The 915MHz RF front-end module is shown in Figure 2.

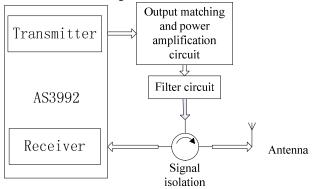


Figure 2. 915MHz RF front-end module

AS3992 internal transmitter can output power to 20dBm.Owing to the recognition distance is short, the chip RF5110 is selected as a power amplifier. The RF5110 is a high-power, high-efficiency power amplifier module, and it can operate in the frequency range 800MHz-950MHz. Its Output Power is +36dBm at 3.5V.

915MHz RFID reader needs two antennas to send and receive or transceiver isolation circuit. A directional coupler circuit was used as an isolation circuit for cost savings. Select 5dB directional coupler RCP890A05 as transceiver isolation circuit. Its operating frequency is about 815-960MHz; insertion loss is about 0.19dB; signal isolation is about 26dB ^[4]. The output signal of amplifier was transmitted to the signal isolation and the reception circuit through the low pass filter. As is shown in Figure 3, transmit power is 30.11dBm measured with the spectrum analyzer.

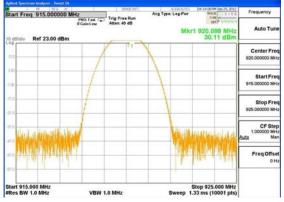


Figure 3. The transmit power spectrogram

B. 2.4GHz RFID module

The design select RF transceiver chip NRF24L01 developed by Nordic as 2.4GHz transceiver. NRF24L01 is a new single chip radio transceiver for the world wide 2.4 -

2.5 GHz ISM band. The transceiver consists of a fully integrated frequency synthesizer, a crystal oscillator, a power amplifier, a demodulator, modulator and Enhanced ShockBurstTM protocol engine ^[5]. What's more, output power and communication channels can be configured through the program. The power consumption is very low, only 9.0mA at an output power of -6dBm and 12.3mA in RX mode. Built-in Power Down and Standby modes makes power saving easily realizable.

2.4GHz RF module can be divided RF matching and power amplification circuit, a low pass filter, signal isolation, and the reception circuit section. Select ASX621 as a power amplifier to increase the identification distance. ASX621 has the characteristics of high linearity, high gain, and high efficiency in a wide frequency range. The amplified signal was transmitted to the filter and signal isolator to achieve the read-write of the RF signal. The operating circuit is shown in Figure 4.

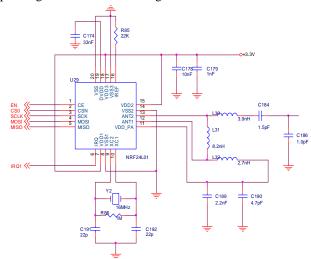


Figure 4. The NRF 24L01 operating circuit

C. Antenna Selection

The antenna can transmit radio frequency signal between tags and readers. It works using what's known as "backscatter technology", and its mode is similar to radar. RF signal generated by the reader radiates to space in a certain direction from the antenna. Through consulting literature, the reader antenna receives power formula is as follows:

$$P_{\psi} = \frac{P * G^2 * \lambda^2 * \sigma}{(4pi)^2 * R^4}$$

In the formula, P means the transmitting power of the reader, G is the antenna gain, R is spacing between tags and the antenna, σ means ability test degrees of target transmitting electromagnetic wave. If you want to increase the read-write distance, you can increase the transmission power, the size of antenna reception and the gain.

When Reader frequency is 915MHz, the polarized microstrip antenna can be used in order to increase the recognition distance. The air is as media; pcb board is as the microstrip ground plane; the radiating portion is the square copper plates, and it respectively feed electricity from the midpoint of each side. When working in the 2.4GHz you can choose microstrip antenna if you want to have a certain communication distance. Microstrip antenna is a kind of antenna that on a thin dielectric substrate, one side was enclosed the metal thin layer as a ground plate; the other surface was made into a certain shape of the metal patch by the method of photolithography etching, and feeding electricity by using a microstrip line [6].

IV. SYSTEM SOFTWARE DESIGN

The reader in reading and writing tags needs to switch in the two operating modes. The demands on processor efficiency and processing speed will get more pressing. Two RF chips communicate with the master controller through SPI serial interface. After receive the communication command from the controller, the RF chips notify the controller that there's data to be processed by using IRQ. The transmission and reception of RF signals can be achieved through the operation of the RF chip registers. For system task scheduling and management of system resources, it is necessary to build uCOS-II operating system on the hardware platform. On this basis, completing the tag reading and writing will enable the development of software more convenient and efficient, and the full play of the processing capability of the controller will. The software design includes hardware initialization, the 915MHz RF's read-write, 2.4GHz RF's read-write and multi-tag anticollision processing.

For the multi-task scheduling, to achieve inter-task communication and synchronization, semaphore and message queues of uCOS-II system can be used. First, establish the 915MHz and 2.4GHz RF module "notice" and "answer" semaphore to achieve synchronization between tasks, and then re-establish message queues which include a certain number of messages for inter-task communication. The software general flowchart is as shown in Figure 5.

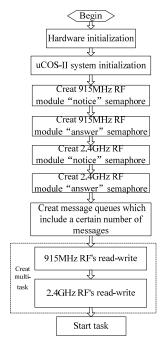


Figure 5. The software general flowchart

A. 915MHz RF's read-write

AS3992's initialization is mainly to complete the configuration of work frequency, modulation parameters and chip register through the parallel interface. AS3992 supports multiple protocols, and the ISO6C protocol ^[7] working mode is selected. The decoding and check had been completed, MCU only need to receive data by parallel or serial interfaces. The Reader identifies all tags though several steps as follows:

- 1) Set the Q parameter values;
- 2) The "select" command is issued to select the label group;
 - 3) Start the inventory cycle;
- 4) Constantly send "QueryRep" and "ACK" commands till all slots of Q value parameter were traversed;
 - 5) Identify all the tags.

B. 2.4GHz RF's read-write

NRF2401 is a highly integrated RF transceiver chip. Its communication protocols have solidified into chips to simplify the development process. Four operating modes including transceiver mode, configuration mode, idle mode and off mode operating mode are determined by PWR_UP register, PRIM_RX register and CE. The ShockBurstTM transceiver mode was selected. In this operating mode, the programming of the system is simple and higher stability. First initialize transceiver port, and then examine whether the received data is correct or not. After that the tags were coded and decoded, and you need anti-collision processing if there are more tags. All configuration of NRF2401 is done via the SPI. NRF2401 initialization is mainly to configure

RF transceiver module mode, the frequency of the channel, the transmission data rate, CRC checksum, crystal frequency, address, etc. The initialization code is as follows:

```
void init_NRF24L01(void)
{
  inerDelay(100);
  CE=0;
  CSN=1;
  SCK=0:
  SPI_Write_Buf(WRITE_REG + TX_ADDR,
TX_ADDRESS, TX_ADR_WIDTH);
  SPI_Write_Buf(WRITE_REG + RX_ADDR_P0,
RX ADDRESS, RX ADR WIDTH);
  SPI_RW_Reg(WRITE_REG + EN_AA, x01);
  SPI_RW_Reg(WRITE_REG + N_RXADDR, 0x01);
  SPI_RW_Reg(WRITE_REG + RF_CH, 0);
  SPI_RW_Reg(WRITE_REG +RX_PW_P0, PLOAD_
WIDTH);
  SPI_RW_Reg(WRITE_REG + RF_SETUP, 0x07);
}
```

That is, NRF2401 works on the ShockBurstTM transceiver mode, channel frequency is 2.4GHz, transmitting power is 1MHZ, transmitting power is the maximum of 0dB, and the length of received data is set to 32 bytes.

V. EXPERIMENTAL RESULTS

First hardware circuit was tested with a multimeter, oscilloscope, etc. After testing, the welding of the chip device pins is no short-circuit and virtual welding, chip operating voltage and the supply voltage is normal, and transmit power is about 30.11dBm measured with the spectrum analyzer. In software, the identification distance of the reader and multiple tags read-write were tested. The identification distance of the reader was more than five meters in the system. Multi-tag anti-collision is effect, and the system is stable and reliable. The result of multi-tag reading is as shown in Figure 6, it shows the tags reading times and tag number.

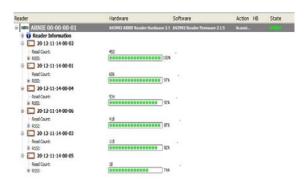


Figure 6. The result of multi-tag reading

VI. CONCLUSION

The RFID reader based on dual-mode is put forward, and the hardware organization and software design of the system are also offered in detail. Being tested, it completes basic read and write operations of the dual-mode radio frequency signal. The identification distance is more than 5m. Besides, the reader has a certain anti-collision ability. It basically achieves the design goals, and has broad market prospects.

ACKNOWLEDGMENT

I would like to extend my sincere gratitude to Tongtong Wang and Changbin Shang, for their hard work on my thesis. I am deeply grateful of their help in the completion of this thesis.

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