Design of Intelligent Access Control System Based on UWB Location Algorithm

MENG Xiang Electrical & Information Engineering College Beihua University Jilin, China 731856651@qq.com

Abstract—The intelligent access control system based on UWB is designed in this paper. The system includes access controller, UWB reader array, electronic lock, power module, video capture modular and master computer. The access controller obtains the card's information by UWB reader array, the staffs' position can be located by UWB location algorithm. By comparing with information obtained by video capture modular, the system can indicates that if there is illegal invader. The location accuracy can reach 0.5m and can identify accurately the invader's position. So the system can used widely in the place which requires fast walk.

Keywords- access controller;UWB reader array;UWB location algorithm; video capture modular

I. INTRODUCTION

With the continuous development and improvement of people's sense of safety and prevention, access control system is more and more widely used in modern office and house. The access control system in market has much disadvantage including structure and function's flaw and inefficiency. So the design of a responsive, powerful, easy to expand intelligent access control system is very important. The intelligent access control system based on UWB is designed in this paper. The system includes access controller, UWB reader array, door magnetism, electronic lock, power module, video capture modular, host computer and master computer. The system has higher location accuracy comparing with other access control system in market.

II. UWB LOCATION ALGORITHM

UWB(Ultra-Wide Band) location algorithm is used in this system because the algorithm has high location accuracy. Its principle is that the measured point sends a signal to the base stations of all reference points, the coordinate can be calculated by the arrival time of signal. According to the geometry principle of location, there will be 3 unknown number if we want to locate a measurement point's three-dimensional coordinate. The signal's time of arrival(TOA) is also unknown, so there are at least 4 reference points, 4 equations should be constructed in order to calculate the solutions. The equation is shown as follows: ZHAO Ying Electrical & Information Engineering College Beihua University Jilin, China Zhaoying_jl@126.com

$$\sqrt{(x - x_i)^2 + (y - y_i)^2 + (z - z_i)^2} = c(t_i - t_0)$$

 $i = 1, 2, 3, 4$ (1)

In the equation,(x,y,z) is the location position's coordinate; (x_i,y_i,z_i) is reference point's coordinate; t_0 is the initial time of sending signal; t_i is signal's arrival time. The measurement point's coordinate can be known if the equation can be solved.

The direct calculation method and the iteration method based on optimization can be used to solve the equation. In the direct calculation method, using time difference, after the substitution, the equations are simplified to obtain solutions. Usually the equations have two solutions and only a desired solution. If the calculated solution has no physical meaning or exceed the measuring range, the solution is regarded as invalid solution. If the two solutions are similar to, then their intermediate value should be taken.

The iteration method based on optimization is that the target function of the location coordinate is written as follows according to the equation(1).

$$f(p) = \sum_{i=1}^{k} (\sqrt{(x-x_i)^2 + (y-y_i)^2 + (z-z_i)^2} - r_i)^2$$
(2)

In formula(1), k is the number of reference points; ri is the distance between i reference point and target point; $p = [x, y, z]^T$ is coordinate of location point. The purpose of optimization is to find the minimum value of target function. The algorithm is shown as follows.

Step1:set a initial point P_0 , a initial matrix B0, let k=0; Step2:calculate g0(g is gradient of targent

function).
$$g_k = \nabla f(p) = \left[\frac{\partial f}{\partial x}, \frac{\partial f}{\partial y}, \frac{\partial f}{\partial z}\right]_{P=P_k}^T;$$

Step3:determine the step size ak by precision one-dimension search,

$$f(P_k + a_k s_k) = \min f(P_k + a_k s_k), a_k \ge 0, \ s_k = -B_k g_k$$

Step4:let
$$P_{k+1} = P_k + a_k s_k$$
, if $\|g_{k+1}\| \leq \varepsilon$, then $P^* = P_{k+1}$; if
not, let $s_k = P_{k+1} - P_k$, $y_k = g_{k+1} - g_k$, obtain Bk+1 by

correction

formula

 $\boldsymbol{B}_{k+1} = \boldsymbol{B}_k - (\boldsymbol{B}_k \boldsymbol{y}_k \boldsymbol{y}_k^T \boldsymbol{B}_k) / \boldsymbol{y}_k^T \boldsymbol{B}_k \boldsymbol{y}_k + \boldsymbol{s}_k \boldsymbol{s}_k^T / \boldsymbol{y}_k^T \boldsymbol{s}_k.$

Step5:let k=k+1, if $k \le$ set value, then return step2; Step6: Exit the loop, then output results.

Matlab program of the iteration method's error analyzing is shown as follows.

light speed = 3e8; AP = [0 0; 0 20; 20 0;20 20]; $num_ap = length(AP);$ $Tag = [15 \ 11];$ num bits = 1000: ts=1/20e9; k = 0;for EbNo = -40:5:-30 k = k + 1;for nloop = 1:1TOA tag = (length(sig) - delay1) * ts; $TOA_1 = (TOA_ap1 + TOA_tag)/2;$ $TOA_2 = Td_ap1_tag2 - TOA_1 + TOA_tag;$ $TOA_3 = Td_ap1_tag3 - TOA_1 + TOA_tag;$ time_dur = $[TOA_1 TOA_2 TOA_3];$ toa_error(k,nloop) = toa(AP,Tag,time_dur,light_speed) end end

plot(-15:5:0, toa_error);

The comparison chart of the direct calculation method and the iteration method's location accuracy is shown in Figure 1.



Figure 1. The comparison chart of the direct calculation method and the iteration method's location accuracy

The TOA of signal from the measurement point to the basic station obey $N(0, \delta^2)$ distribution. The repeated measurement average time will close to the true value, so the time averaging method is used to improve the location accuracy. The TOA of 10 times is averaged at the base of iteration method (5 choose 4), the location accuracy can reach 0.6m. The method can improve 25% comparing with the normal iteration method. The location accuracy is higher if averaging times increase. The accuracy comparison chart of different number for time-average is shown in Figure 2.

Figure 2 shows that the location accuracy can reach 0.2m, but the price is the increasing of measurement number, the location time and the power consumption., so average time of 20 times is used in this paper.



Figure 2. The accuracy comparison chart of different number for timeaverage

III. DESIGN OF HARDWARE

The diagram of access controller's hardware circuit is shown in Figure 3. The system includes access controller, UWB reader array, door magnetism, electronic lock, power module, video capture modular, host computer and master computer. ARM chip S3C6410 is chosen as the main microcomputer because it had better function and fast performance speed. First, access controller initializes external equipments, including UWB reader array, communication, door magnetism, database and so on. Then access controller goes into monitoring state, once detects the card to be present, the controller reads card's information by UWB reader array. The card's information includes ID, position, number and so on. The card's number is the legal stuff's number. By comparing with information obtained by video capture modular, the system can indicates that if there is illegal invader. If there is illegal invader, the controller locates the invader's position by UWB location algorithm, and marks the position of the invader at the video image, and at the same time the controller gives alarm. The relevant information will be saved in the local and remote database.



Figure 3. The diagram of access controller's hardware circuit

In the access control system, controller communicates with the master computer by LAN network. Controller sends the information obtained to the master computer and at the same time executes the dynamic command received. The master computer can connect with remote database by TCP/IP protocol. The access controller and the UWB reader array communicate by RS485 bus. JTAG interface is mainly used to test inner device, emulation and debug, and it is a simple and effective method of develop and debug. The JTAG interface's connection circuit with S3C2440 is shown in Figure 4.



Figure4. JTAG interface's connection circuit with ARM

ARM chip S3C6410 is chosen as the main microcomputer, the diagram of main microcomputer is shown in Figure 5. 256M DDR memory and 256M Flash memory are expand, and at the same time 2 COM interface and 1 LAN interface are designed. S3C6410 has ARM 1176TEF-S kernel, its highest frequency can reach 667MHz, so it can completely satisfy the requirement of access control system at speed.



Figure 5. The diagram of main microcomputer

IV. DESIGN OF SOFTWARE

The access controller's flow diagram of main program is shown in Figure 6.



Figure 6. The access controller's flow diagram of main program

V. CONCLUSION

The intelligent access control system is designed in this paper. The system includes access controller, UWB reader array, door magnetism, electronic lock, power module, video capture modular, host computer and master computer. Then access controller reads card's information by UWB reader array. By comparing with information obtained by video capture modular, the system can indicates that if there is illegal invader. If there is illegal invader, the controller locates the invader's position by UWB location algorithm, and marks the position of the invader at the video image, and at the same time the controller gives alarm. UWB(Ultra-Wide Band) location algorithm is used in this system and the location accuracy can obtain 0.5m. The hardware diagram and corresponding software flow diagram are given in detail. The system choose ARM chip S3C2440 as main microcomputer, so the system had faster performance and better stability. The master computer and the access controller communicate by LAN network. The computer managerial interface is simple and operates easily. So the system has extensive application prospect.

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