Design of Multi-standard Low Noise Amplifier based on Active Inductor

Weiyin Wang, Hailian Liang, Linna Zhao, Xiaofeng Gu^{*} Key Laboratory of Advanced Process Control for Light Industry (Ministry of Education) Department of Electronic Engineering; Jiangnan University Wuxi, China

xgu@jiangnan.edu.cn

Abstract—A current-controlled multi-standard low-noise amplifier (LNA) has been proposed based on the active inductor. The novel folded-cascode circuit topology with feedback resistance ensures good quality factor and linearity of the active inductor. The current-controlled inductance makes the LNA work well with various wireless communication standards such as TD-SCDMA, CDMA2000, WCDMA and Bluetooth. Simulation results indicate that the multi-standard LNA designed using 0.18 μ m CMOS technology has a high gain (>11.6 dB), low noise figure (<1.851 dB), good input impedance matching (<-26.78 dB).

Keywords-multi-standard; LNA; active inductor; currentcontrolled;

I. INTRODUCTION

With the rapid development of the 3G cellular system, it has become essential to integrate various wireless communication standards into a cell phone. Due to the increasing demand on global roaming and all-in-one cell phones, the interest in developing multi-standard transceivers has been increasing. Currently, the most popular standards in China include TD-SCDMA, CDMA2000, WCDMA and Bluetooth. Operating frequencies of these standards are listed in Table 1. Many researches have been done on multistandard low noise amplifier (LNA). Recently, the ultrawideband (3.1-10.6 GHz) LNA has also been designed [1], but it has a high noise figure (NF) and bad impedance matching for the 3G cellular system. In the dual-input pseudo-switch radio frequency (RF) LNA, RF switch introduces large noise when shifted among various matching circuits. In this work, we propose a novel multi-standard LNA based on the active inductor. The current-controlled inductance makes the LNA work well with various standards, while the folded-cascode structure with feedback resistance improves the overall performance of LNA.

TABLE I. OPERATING FREQUENCIES OF VARIOUS STADARDS

Standard	Frequency (MHz)
WCDMA	2130-2145
CDMA2000	2110-2125
TD-SCDMA	2010-2025
Bluetooth	2400

II. THEORY AND DESIGN

A. Current-controled Active Inductor

As shown in Fig. 1, active inductors usually adopt the Gyrator-C structure [2-4], using CMOS transistors to achieve the spiral inductor function. The advantages of this structure include easy tuning of inductance with high quality factor (Q) and occupying less chip area than conventional spiral inductors. According to Fig. 1, the equivalent inductance (l_{eq}) and the equivalent resistor (r_{eq}) can be expressed as

$$l_{eq} = \frac{g_{m2}g_{m3}c_{gs2} + \omega^2 c_{gs2}^2 c_{gs3}(Rg_{ds3} + 1)}{g_{m2}^2 g_{m3}g_{m1} + \omega^2 g_{m3}g_{m1} c_{gs2}^2}, \qquad (1)$$

$$r_{eq} = \frac{g_{m2}g_{ds3}g_{ds1} + \omega^2(g_{m3}c_{gs2}^2 - g_{m2}c_{gs2}c_{gs3}(Rg_{ds3} + 1))}{g_{m2}^2g_{m3}g_{m1} + \omega^2g_{m3}g_{m1}c_{gs2}^2}, (2)$$

where g_{m1} , g_{m2} and g_{m3} are the transconductance of M₁, M₂ and M₃, respectively, C_{gs2} and C_{gs3} are the parasitic gate-tosource capacitance of M₂ and M₃, respectively, g_{ds3} is the drain conductance of M₃, ω is the operating frequency, and *R* is the feedback resistance. According to (1) and (2), we can control the values of l_{eq} and r_{eq} by adjusting g_{m1} , g_{m2} and g_{m3} .



Figure 1. Schematic circuit diagram of active inductor

Published by Atlantis Press, Paris, France. © the authors 0830 The relation between the transconductance (g_m) and the bias current (*I*) of MOS transistor can be expressed as

$$g_m = \frac{2I}{V_{GS} - V_{TH}},$$
 (3)

where V_{GS} is the gate-source voltage and V_{TH} is the threshold voltage. According to (3), g_m can be controlled by the bias current to meet different communication standards. In the circuit shown in Fig. 1, g_{m1} and g_{m3} change with I_1 while g_{m2} changes with I_2 .

B. Multi-standard LNA

As shown in Fig. 2, the designed multi-standard LNA circuit topology adopts an inductor-degenerated common source structure [5-7], in which the proposed current-controlled active inductors are used for impedance matching with different standards.



Figure 2. Multi-standard LNA based on active inductor

The input impedance (Z_{in}) of the LNA can be expressed as

$$Z_{in} \approx s(L_s + L_g) + \frac{1}{sC_{gs1}} + \frac{g_{m1}}{C_{gs1}} L_s, \qquad (4)$$

where C_{gs1} is the parasitic capacitance of M₁, and $s=j\omega$. To obtain a real input impedance of 50 Ω , the following equations must be satisfied:

$$\omega(L_{g}+L_{s}) = \frac{1}{\omega C_{gs}}, \qquad (5)$$

$$Z_{in} = \frac{g_m}{C_{gs}} L_s = 50 \,\Omega \,. \tag{6}$$

The noise figure of the multi-standard LNA is

$$NF \approx 1 + \frac{1}{R_s} \left(\frac{\omega L_g}{Q_{Lg}} + \frac{\omega L_s}{Q_{Ls}} \right) + \gamma g_{d0} R_s \frac{\omega C_{gs1}}{g_{m1}} + \frac{(\omega L_s)^2}{R_s R_d}, \quad (7)$$

where R_s is the source resistance (usually 50 Ω), R_d is the load resistance, g_{d0} is the drain conductance of M₁ at zero bias, Q_{Lg} and Q_{Ls} are quality factors of the inductors L_s and L_g , respectively, and γ is the transfer constant. The channel thermal noise of M₁ and the thermal noise from load resistor R_d are also considered.

Due to the use of R_d , the parasitic effects will make the output impedance (Z_{out}) unstable at high frequencies. To solve this problem, *RC* resonant network is adopted in the multi-standard LNA. Z_{out} can be expressed as

$$Z_{out} = \frac{1}{sC_{out}} + \frac{sR_dL_d}{R_d + sL_d},$$
 (6)

where C_{out} is the load capacitor, and L_d is the load inductor. When sC_{out} and sL_d are much larger than 1, Z_{out} is approximately equal to R_d , suggesting that the output impedance is stable (not changing with operating frequency).

The linearity of the designed LNA is proportional to the overdrive voltage of M_1 when M_1 and M_2 are biased in the high saturation region and kept far from the triode region. The linearity represented by the input third-order intercept point (*IIP*₃) is

$$IIP_3 \approx V_{GS} - V_{TH}, \qquad (8)$$

where V_{GS} is the gate-source voltage of M₁ and V_{TH} is the threshold voltage. According to (4), L_g and L_s should vary correspondingly with different standards. However, tests have demonstrated that even keeping L_s as a constant, different standards can also be satisfied. The values of L_g are listed in Table 2 for different standards.

TABLE II. ACTIVE INDUCOTOR FOR DIFFERENT STANDERS

Standard	$L_g(\mathbf{nH})$
WCDMA	27.27
CDMA2000	27.83
TD-SCDMA	30.66
Bluetooth	21.53

III. SIMULATION RESULTS

A. Quality Factor of Active Inductor

Fig. 3 shows the obtained Q for the proposed active inductor in the frequency range of 1.5 GHz to 2.8 GHz. A maximum Q of 63.2 is reached at 2.1 GHz.

B. S Parameters of Multi-standard LNA

The power gain (S_{21}) of the multi-standard LNA is shown in Fig.4 for different standards. S_{21} exceeds 11.6 dB



63.5

63.0 62.5 62.0 0 61.5 61.0 60.5 1.4 1.8 2.0 2.2 2.4 2.6 2.8 3.0 1.6 f/GHzFigure 3. Quality factor of the active inductor 18 3 17 Power Gain S21/dB 16 Mar Mar 15 14 13 TDSCDMA 12 CDMA2000 WCDMA 11 BLUETOOTH 10 2.0 2.1 2.2 2.3 2.4 2.5 f/GHz

Figure 4. Power gain (S21)

The input matching (S_{11}) is shown in Fig. 5. S_{11} is well below -54.01 dB. The best matching (-55.16 dB) is obtained for TD-SCDMA at 2.017 GHz.



Fig. 6 illustrates the changing tendency of the output matching (S_{22}) , which is well below -26.78 dB for all

desired frequencies. The minimal reflection at the load can ensure the efficient power transfer.

Fig. 7 shows the reverse isolation characteristics (S_{12}). A minimum value of about -47.18 dB in the entire frequency range offers good stability to the design LNA.



C. Noise figure of multi-standard LNA



Figure 8. Noise figure of the multi-standard LNA

Fig.8 gives the noise figure of the designed LNA, which is smaller than 1.851 dB at required frequencies of the four standards. A minimum noise figure of 1.499 dB is obtained at 2.137 GHz for WCMDA.

IV. SUMMARY

A multi-standard LNA has been designed using 0.18 µm CMOS technology based on the active inductor. The performance of the LNA is analyzed using Cadence. Simulation results of power gain, noise figure, input and output matching have demonstrated that the designed LNA meets the requirement of the four standards of CDMA2000, TD-SCDMA, WCDMA and Bluetooth. The tunable feature of the LNA is achieved by using the active inductor, which also helps to reduce the chip area.

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